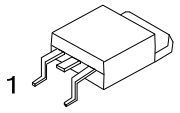
UTT16P10Z

Preliminary | ***Power MOSFET***

-100V, -16A P-CHANNEL POWER MOSFET



TO-252

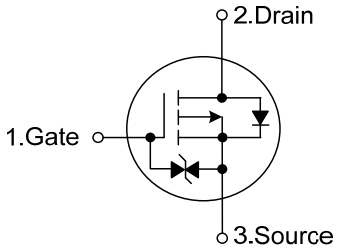
■ DESCRIPTION

The UTC **UTT16P10Z** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and a minimum on-state resistance. It can also withstand high energy in the avalanche.

■ FEATURES

* $R_{DS(ON)} \leq 0.21\ \Omega$ @ V_{GS}=-10V, I_D=-10A
* $R_{DS(ON)} \leq 0.22\ \Omega$ @ V_{GS}=-4.5V, I_D=-6.5A
* High Switching Speed

■ SYMBOL



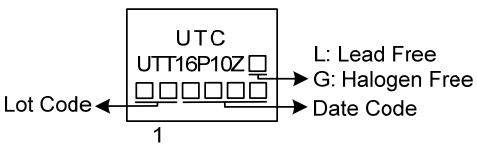
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT16P10ZL-TN3-R	UTT16P10ZG-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT16P10ZG-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_J=25°C, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			V_{DSS}	-100	V
Gate-Source Voltage			V_{GSS}	±20	V
Drain Current	Continuous, V_{GSS}@-10V	T_C=25°C	I_D	-16	A
		T_C=100°C		-9.8	A
	Pulsed (Note 2)		I_{DM}	-32	A
Avalanche Energy	Repetitive (Note 3)		E_{AS}	153	mJ
Peak Diode Recovery dv/dt (Note 4)			dv/dt	10.5	V/ns
Power Dissipation (T_C=25°C)			P_D	45	W
Junction Temperature			T_J	-55 ~ +150	°C
Storage Temperature			T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive rating; pulse width limited by max. junction temperature.
3. L=30mH, IAS=-3.2A, VDD= -50V, RG=25Ω, Starting TJ=25°C.
4. I_{SD} ≤ -16A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	110	°C/W
Junction to Case	θ_{JC}	2.77 (Note)	°C/W

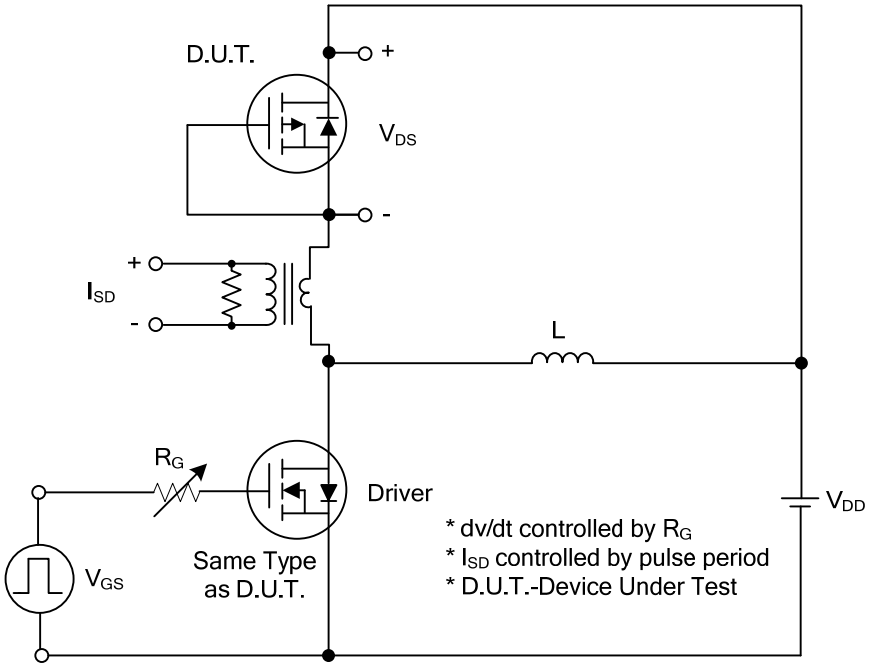
Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

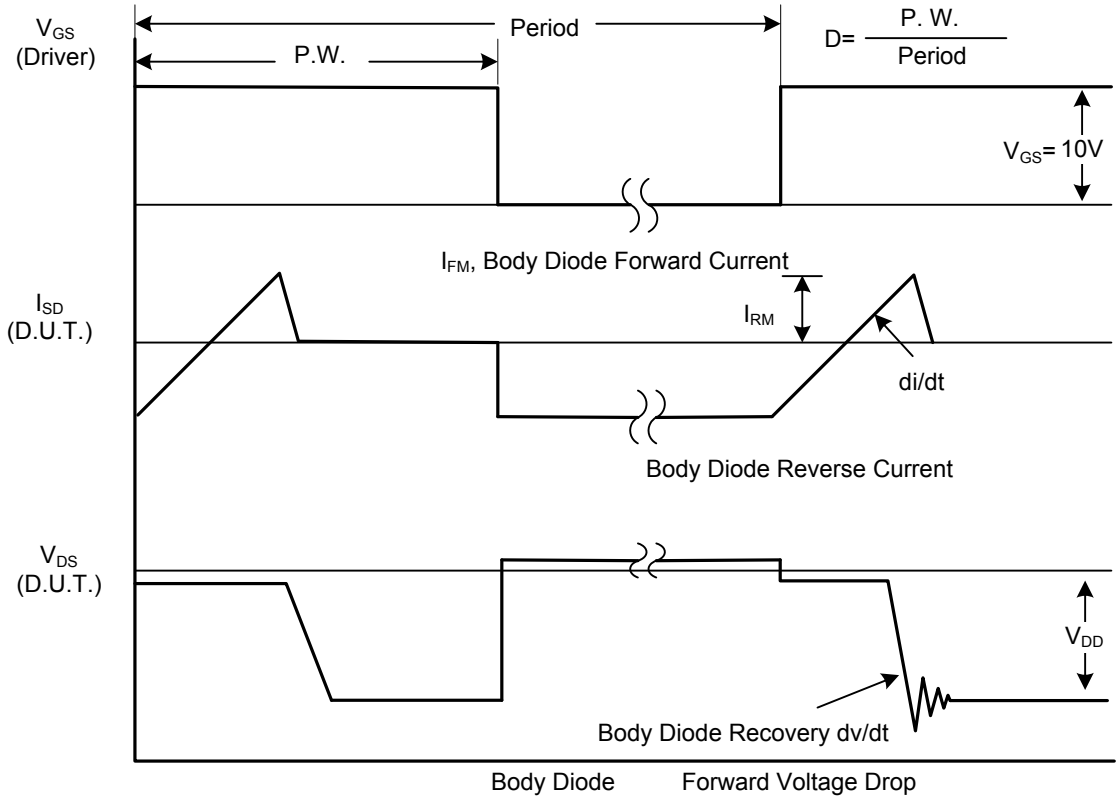
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=-250μA, V_{GS}=0V	-100			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=-100V, V_{GS}=0V,			-1	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V			+10	uA
	Reverse		V_{GS}=-20V			-10	uA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=-10V, I_D=-10A		0.14	0.21	Ω
			V_{GS}=-4.5V, I_D=-6.5A		0.16	0.22	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{DS}=-25V, V_{GS}=0V, f=1.0MHz		1460		pF
Output Capacitance		C_{OSS}			82		pF
Reverse Transfer Capacitance		C_{RSS}			62		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	VDS=-80V, VGS=-10V, ID=-16A(Note 1, 2)		25		nC
Gate to Source Charge		Q_{GS}			5		nC
Gate to Drain ("Miller") Charge		Q_{GD}			5.2		nC
Turn-ON Delay Time		$t_{D(ON)}$	VDD=50V, VGS=10V, ID=-16.0A,RG=6Ω(Note 1, 2)		6.6		ns
Rise Time		t_R			18		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			38		ns
Fall-Time		t_F			24		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				-16	A
Maximum Body-Diode Pulsed Current		I_{SM}	(Note 1)			-32	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=-16A, V_{GS}=0V (Note 2)			-2.6	V
Body Diode Reverse Recovery Time (Note 1)		t_{rr}	I_S=-16A, V_{GS}=0V, dI_F/dt=100A/μs		53		ns
Body Diode Reverse Recovery Charge		Q_{rr}			70		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

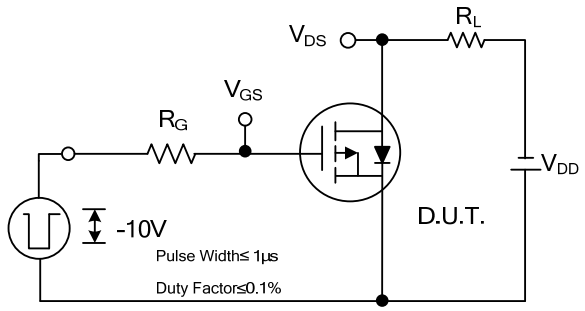


Peak Diode Recovery dv/dt Test Circuit

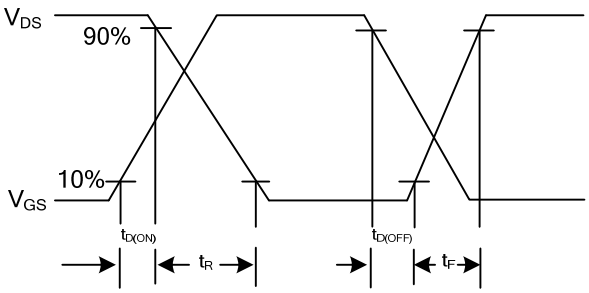


Peak Diode Recovery dv/dt Waveforms

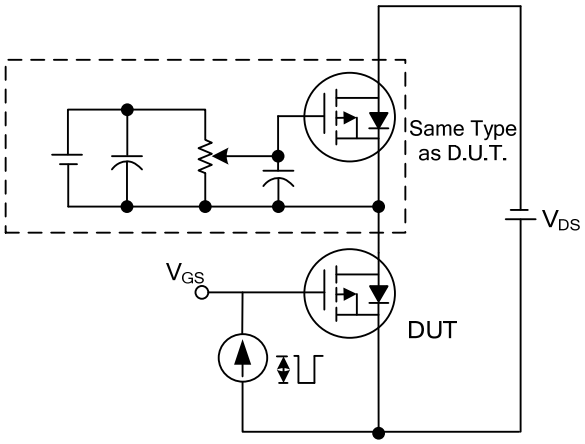
■ TEST CIRCUITS AND WAVEFORMS



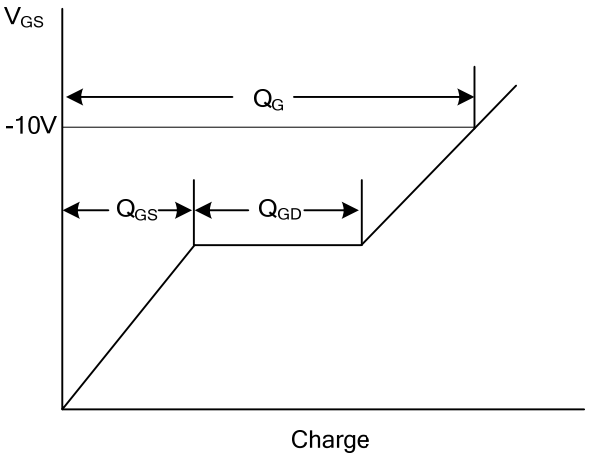
Switching Test Circuit



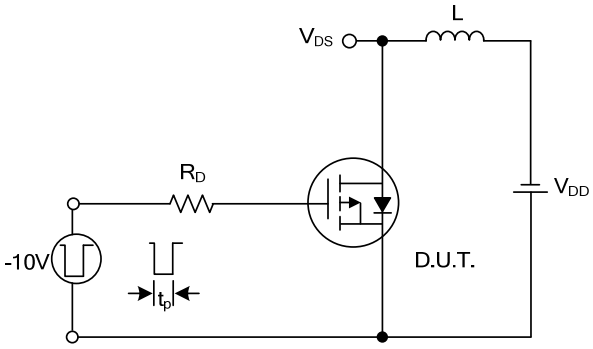
Switching Waveforms



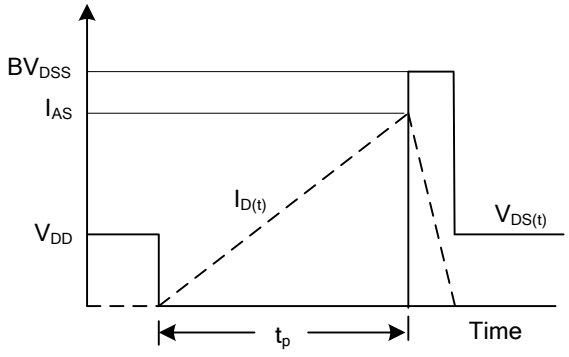
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms