

**UT1D5P06****Power MOSFET****-1.5A, -60V P-CHANNEL
POWER MOSFET****DESCRIPTION**

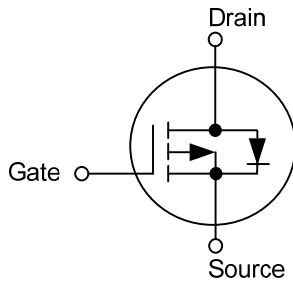
The UTC **UT1D5P06** is a P-channel enhancement power MOSFET using UTC's advanced technology to provide the customers with perfect $R_{DS(ON)}$ and low gate charge.

FEATURES

* $R_{DS(ON)} \leq 0.23 \Omega$ @ $V_{GS} = -10V$, $I_D = -0.9A$

$R_{DS(ON)} \leq 0.27 \Omega$ @ $V_{GS} = -4.5V$, $I_D = -0.8A$

* High switching speed

SYMBOL**ORDERING INFORMATION**

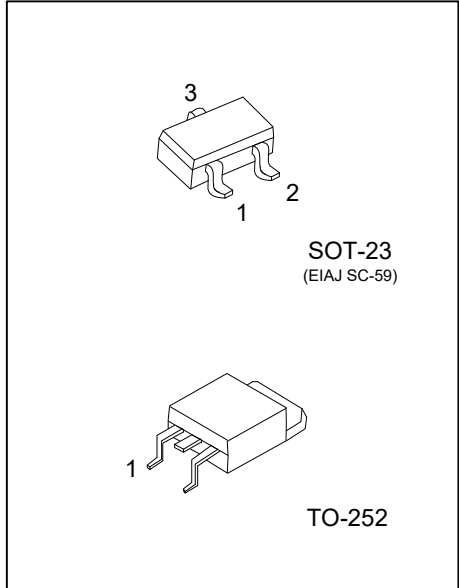
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT1D5P06L-AE3-R	UT1D5P06G-AE3-R	SOT-23	G	S	D	Tape Reel
UT1D5P06L-TN3-R	UT1D5P06G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

UT1D5P06G-AE3-R (1) Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel (2) AE3: SOT-23, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

SOT-23	TO-252



SOT-23
(EIAJ SC-59)

TO-252

■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-60	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current	Continuous	I_D	-1.5	A
Pulsed Drain Current	Pulsed (Note 2)	I_{DM}	-3.0	A
Single Pulsed Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	6.5	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.7	V/ns
Power Dissipation ($T_A=25^{\circ}\text{C}$)	SOT-23	P_D	0.35	W
	TO-252		2.2	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=0.1\text{mH}$, $I_{AS}=-11\text{A}$, $V_{DD}=-50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

4. $I_{SD}\leq -1.5\text{A}$, $di/dt \leq 200\mu\text{A/s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J=25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	SOT-23	θ_{JA}	357	$^{\circ}\text{C/W}$
	TO-252		56.82	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

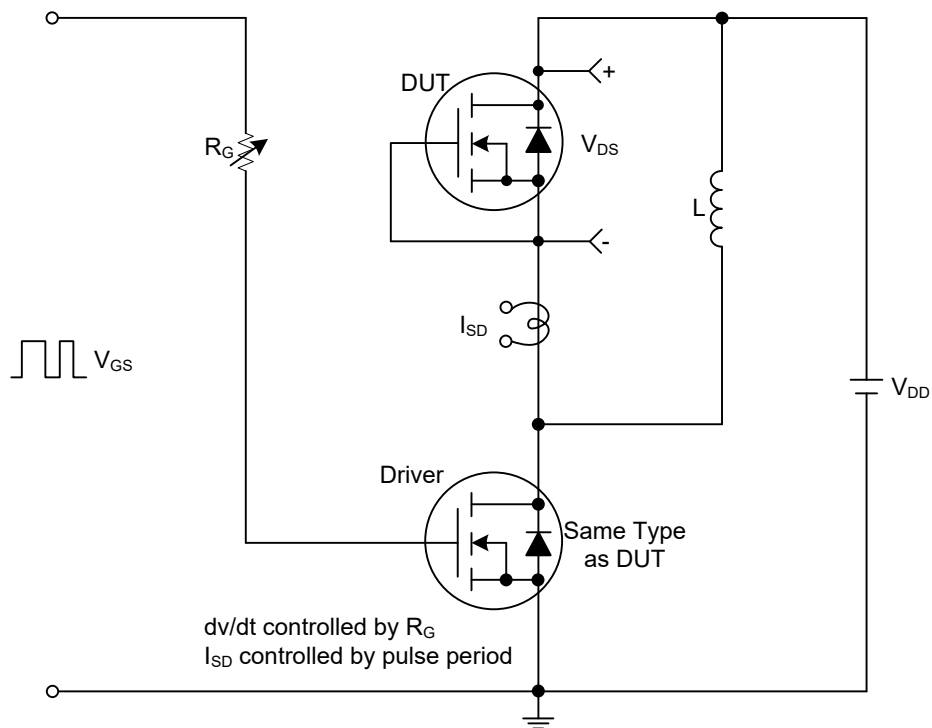
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0 V, I _D =-250μA	-60			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{DS} =0V, V _{GS} =+20V			100	nA
	Reverse		V _{DS} =0V, V _{GS} =-20V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} =-10V, I _D =-0.9A			0.23	Ω
			V _{GS} =-4.5V, I _D =-0.8A			0.27	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{DS} =-25V, V _{GS} =0V, f=1.0MHz		350		pF
Output Capacitance		C _{OSS}			32		pF
Reverse Transfer Capacitance		C _{RSS}			22		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =-48V, V _{GS} =-10V, I _D =-1.5A (Note 1, 2)		13		nC
Gate Source Charge		Q _{GS}			3		nC
Gate Drain Charge		Q _{GD}			1.5		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}	V _{DD} =-30V, V _{GS} =-10V, I _D =-1.5A, R _G =3Ω (Note 1, 2)		4		ns
Turn-ON Rise Time		t _R			17		ns
Turn-OFF Delay Time		t _{D(OFF)}			19		ns
Turn-OFF Fall-Time		t _F			17		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				1.5	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				3	A
Diode Forward Voltage (Note 1)		V _{SD}	I _S =-1.5A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time(Note 1)		t _{rr}	I _S =-1.5A, V _{GS} =0V,		16		ns
Body Diode Reverse Recovery Charge		Q _{rr}	dI _F /dt=100A/μs		12		nC

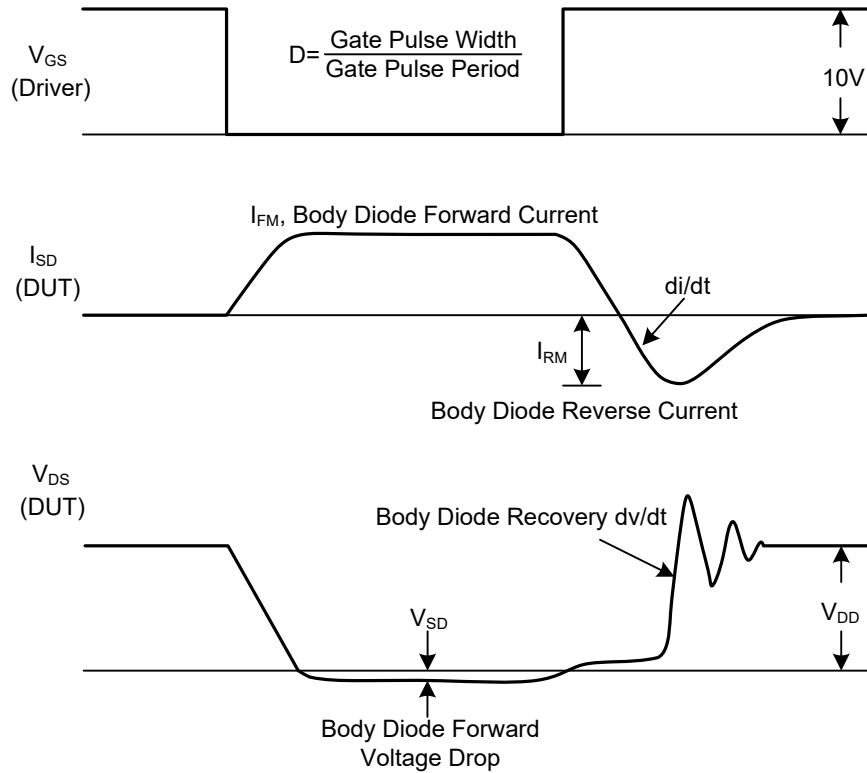
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycles≤2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



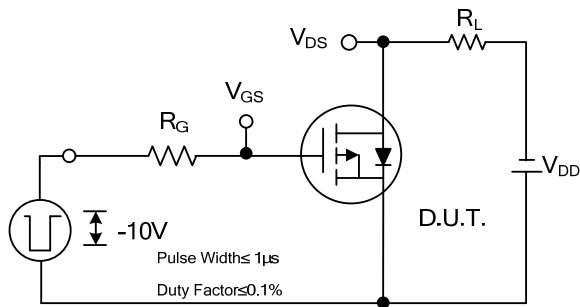
Peak Diode Recovery dv/dt Test Circuit



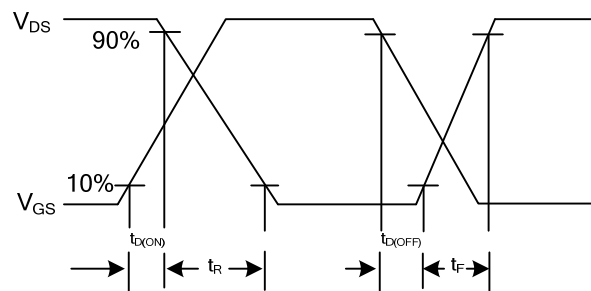
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

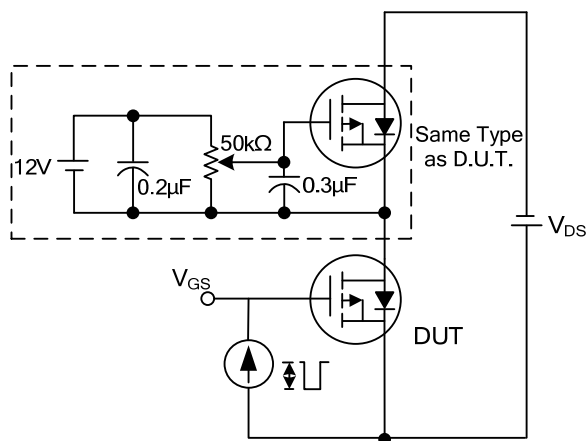
■ TEST CIRCUITS AND WAVEFORMS



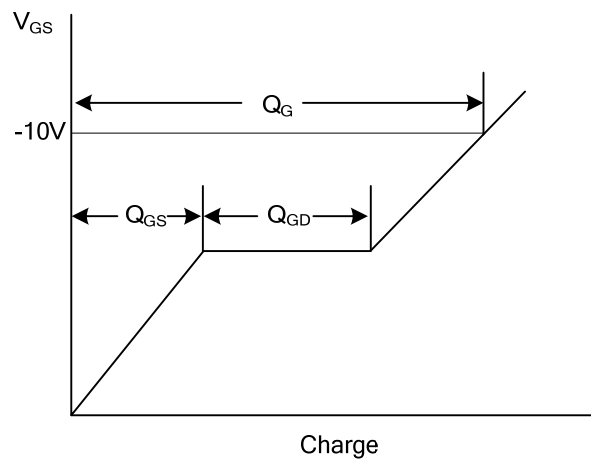
Switching Test Circuit



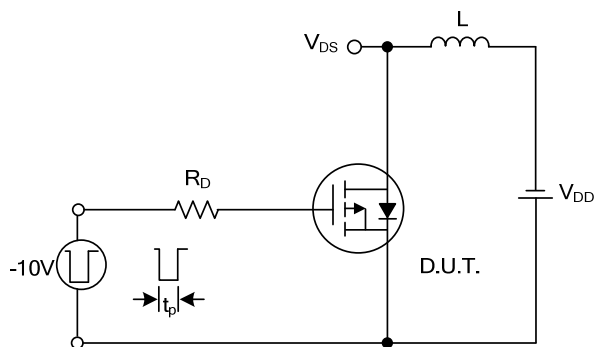
Switching Waveforms



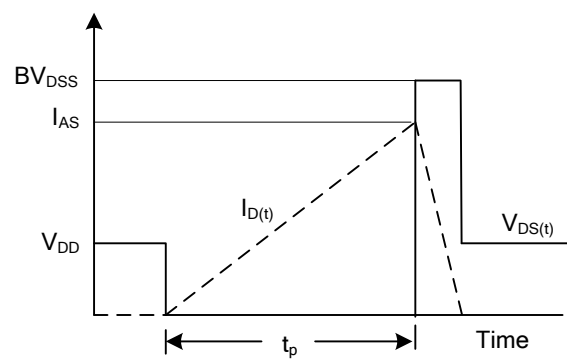
Gate Charge Test Circuit



Gate Charge Waveform

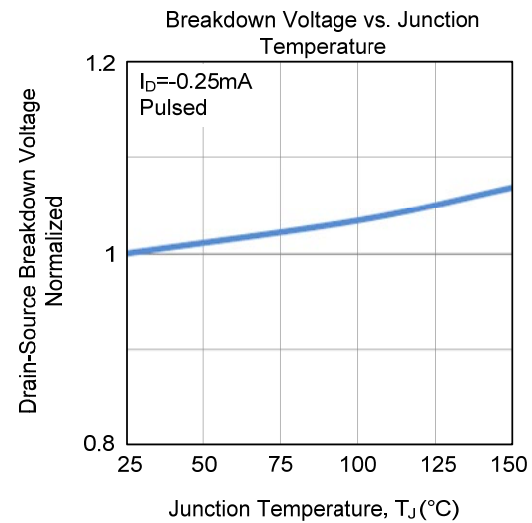
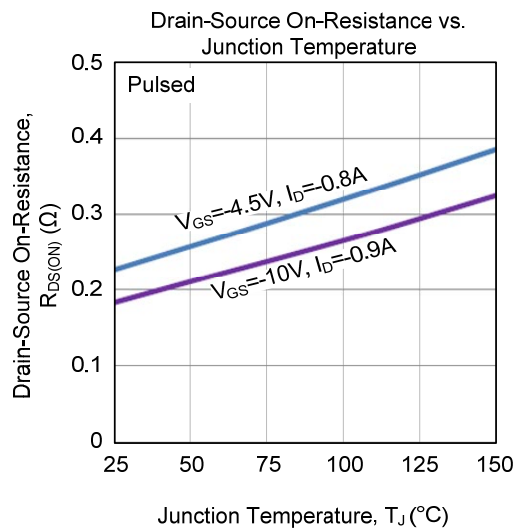
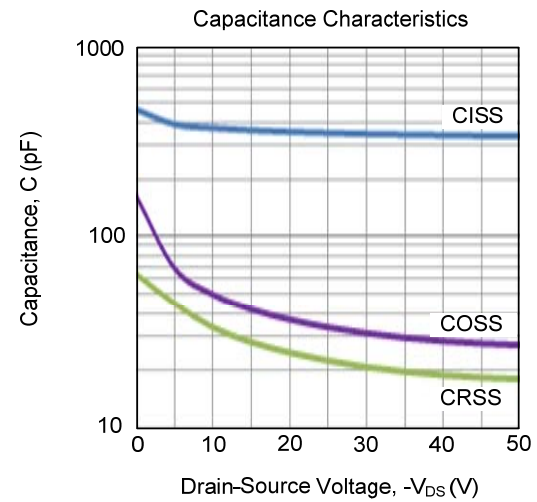
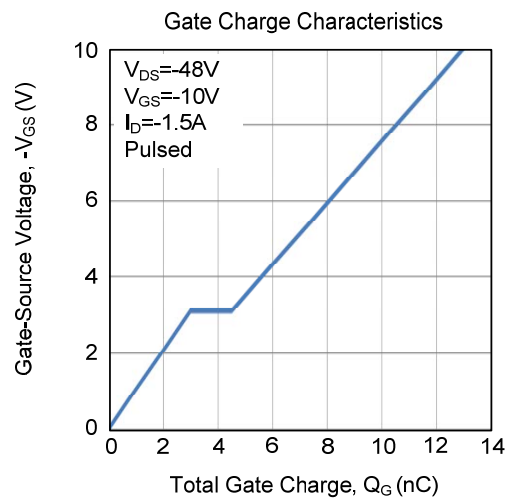
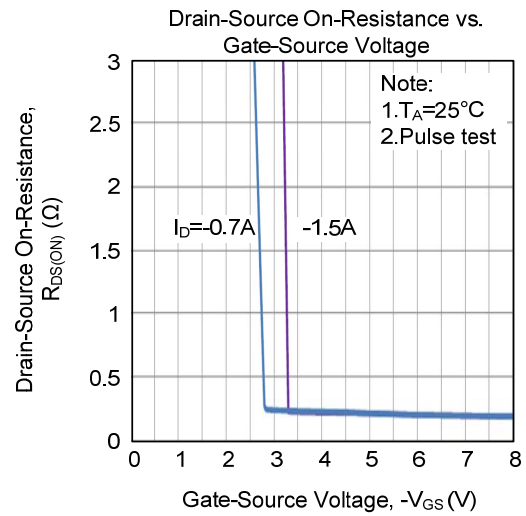
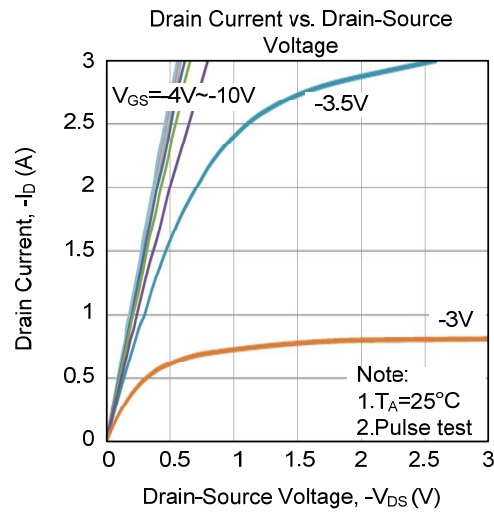


Unclamped Inductive Switching Test Circuit

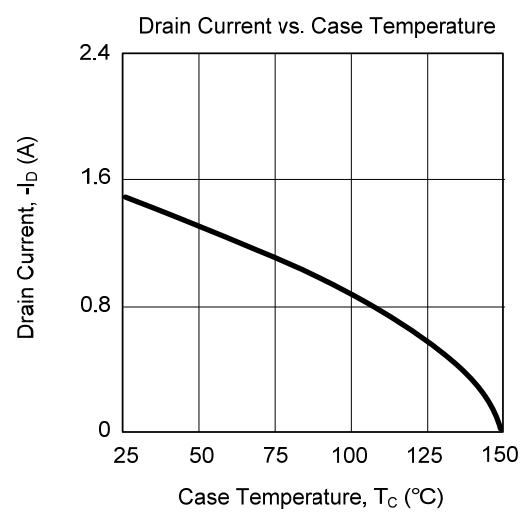
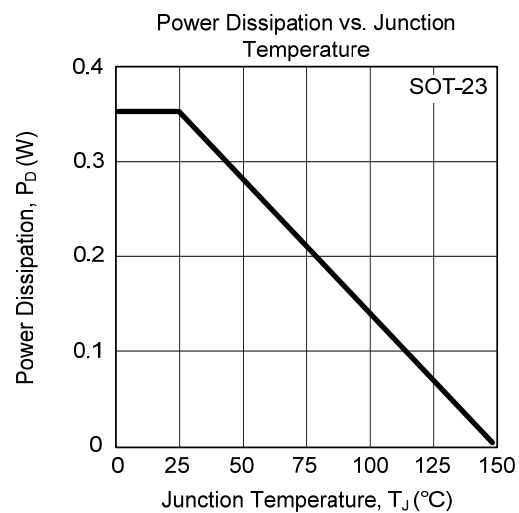
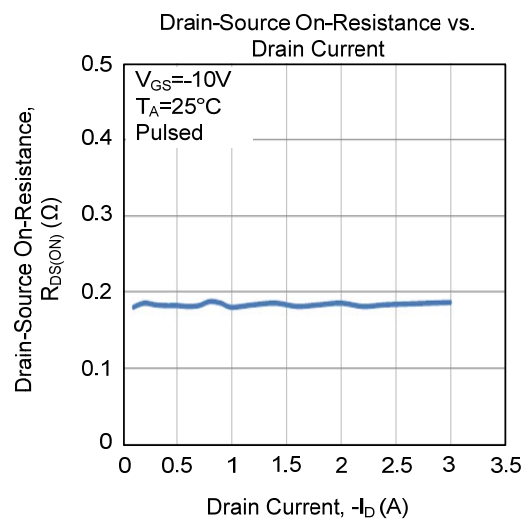
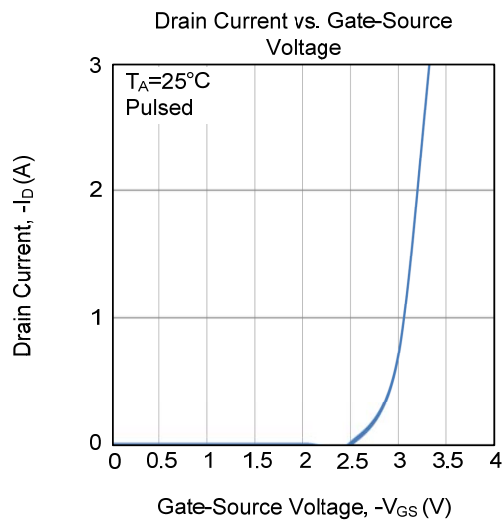
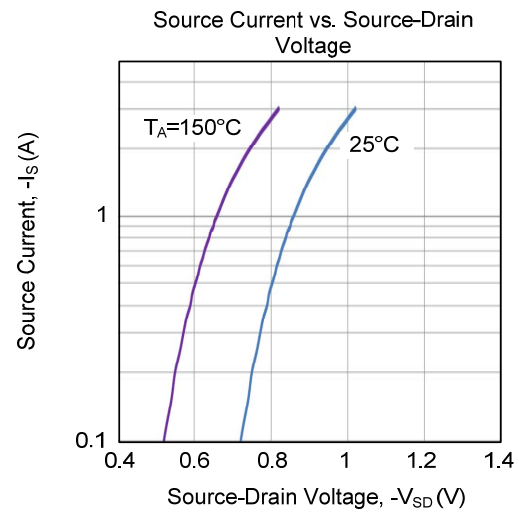
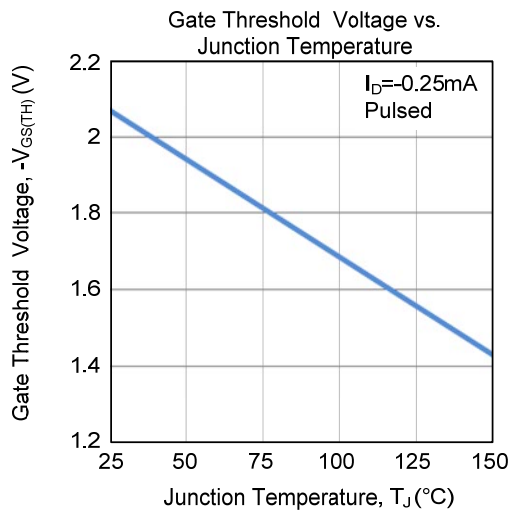


Unclamped Inductive Switching Waveforms

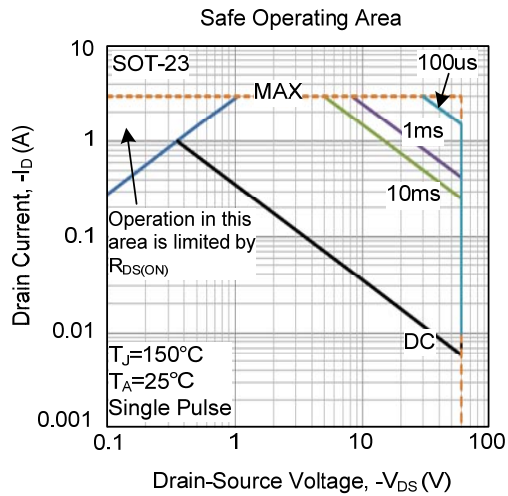
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



Note: 250°C/W when mounted on a 0.02 in² pad of 2 oz. copper.

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