

## UNISONIC TECHNOLOGIES CO., LTD

### UT02N03VZ

#### **Preliminary**

#### **Power MOSFET**

## 0.2A, 30V N-CHANNEL POWER MOSFET

#### ■ DESCRIPTION

The UTC **UT02N03VZ** employs advanced MOSFET technology and features low gate charge while maintaining low on-resistance.

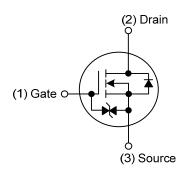
Optimized for switching applications, this device improves the overall efficiency of DC/DC converters and allows operation to higher switching frequencies.

#### **■** FEATURES

- \*  $R_{DS(on)} \le 1.5 \Omega$  @  $V_{GS}=10V$ ,  $I_D=100mA$   $R_{DS(on)} \le 2.0 \Omega$  @  $V_{GS}=4.5V$ ,  $I_D=100mA$   $R_{DS(on)} \le 4.0 \Omega$  @  $V_{GS}=2.5V$ ,  $I_D=10mA$
- \* Low Capacitance
- \* Low Gate Charge
- \* Fast Switching Capability
- \* With ESD protection

# 3 SOT-23-3 (JEDEC TO-236)

#### SYMBOL



#### ORDERING INFORMATION

Note: Pin Assignment: G: Gate

Ordering Number		Daalsana	Pin Assignment			Dealdean	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT02N03VZL-AE2-R	UT02N03VZG-AE2-R	SOT-23-3	G	S	D	Tape Reel	
UT02N03VZL-AL3-R	UT02N03VZG-AL3-R	SOT-323	G	S	D	Tape Reel	

S: Source

D: Drain

UT02N03VZG-AE2-R
(1)Packing Type
(1) R: Tape Reel
(2) AE2: SOT-23-3, AL3: SOT-323
(3)Green Package
(3) G: Halogen Free and Lead Free, L: Lead Free

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MARKING



#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C ,unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current	Continuous	ID	0.2	Α
	Pulsed (Note 2)	I <sub>DM</sub>	0.4	Α
Power Dissipation (T <sub>A</sub> =25°C)	SOT-23-3	Б	0.3	W
	SOT-323	P <sub>D</sub>	0.2	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

#### ■ THERMAL DATA

PARAMETER_		SYMBOL	RATING	UNIT	
Junction to Ambient	SOT-23-3	0	416	°C/W	
	SOT-323	$\theta_{JA}$	625	°C/W	

Note: Device mounted on FR-4 substrate P<sub>C</sub> board, 2oz copper, with 1inch square copper plate.

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

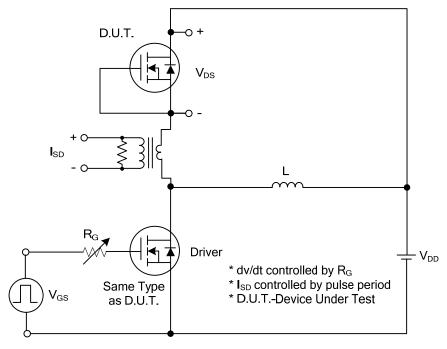
PARAMETER		SYMBOL	TEST CONDITIONS N		TYP	MAX	UNIT	
OFF CHARACTERISTICS					-			
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =0V	30			V	
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Source Leakage Current	Forward		V <sub>DS</sub> =0V, V <sub>GS</sub> =+20V			10	μΑ	
	Reverse	Igss	V <sub>DS</sub> =0V, V <sub>GS</sub> =-20V			-10	μΑ	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5		1.5	V	
			V <sub>GS</sub> =10V, I <sub>D</sub> =100mA			1.5	Ω	
Static Drain-Source On-State Re	Static Drain-Source On-State Resistance		V <sub>GS</sub> =4.5V, I <sub>D</sub> =100mA			2.0	Ω	
			V <sub>GS</sub> =2.5V, I <sub>D</sub> =10mA	<sub>IS</sub> =2.5V, I <sub>D</sub> =10mA		4.0	Ω	
DYNAMIC PARAMETERS								
Input Capacitance		Ciss			14		pF	
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz		9		pF	
Reverse Transfer Capacitance		Crss			4		pF	
SWITCHING PARAMETERS								
Total Gate Charge (Note 1)		Q <sub>G</sub>	\/=24\/\\/=40\/\ 0.24		7		nC	
Gate to Source Charge		Q <sub>GS</sub>	V <sub>DD</sub> =24V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.2A (Note 1, 2)		1		nC	
Gate to Drain Charge		$Q_{GD}$	(Note 1, 2)		0.6		nC	
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			0.2		ns	
Rise Time		$t_R$	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.2A,		18		ns	
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =3Ω (Note 1, 2)		6.4		ns	
Fall-Time		t⊧			20		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Continuous Drain-Source Diode		Is				0.2	Α	
Forward Current						0.2	^	
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	Is=0.2A, V <sub>G</sub> s=0V			1.4	V	
(Note 1)						1.4	V	

Notes: 1. Pulse Test: Pulse width  $\leq$  300 $\mu$ s, Duty cycle  $\leq$  2%.

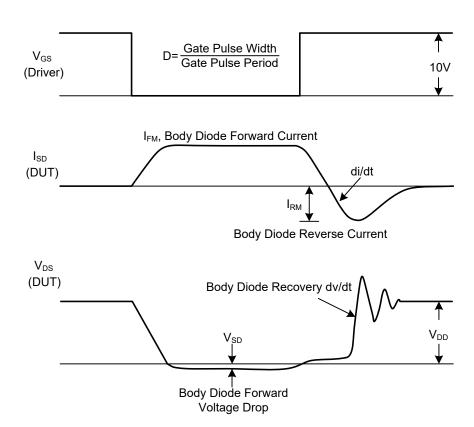
<sup>2.</sup> Essentially independent of operating temperature.



#### ■ TEST CIRCUITS AND WAVEFORMS

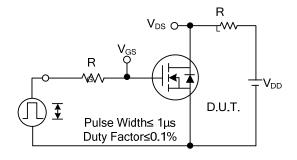


Peak Diode Recovery dv/dt Test Circuit

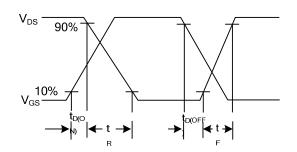


Peak Diode Recovery dv/dt Waveforms

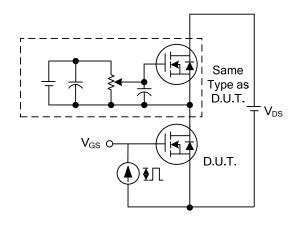
#### ■ TEST CIRCUITS AND WAVEFORMS



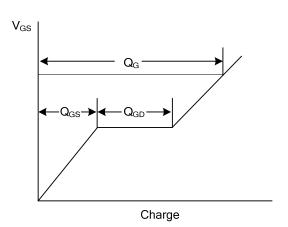
Switching Test Circuit



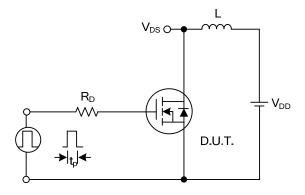
Switching Waveforms



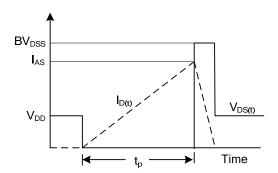
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



**Unclamped Inductive Switching Waveforms** 

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