ULTRA LOW CAPACITANCE TVS DIODE



X3DFN0603-2

■ DESCRIPTION

The UTC **UESD5V0U1B03** is ElectroStatic Discharge (ESD). protection diode in leadless ultra small Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

■ FEATURES

* Bi-directional configurations
* 13Watts peak pulse power (t_p = 8/20μs)
* Solid-state silicon-avalanche technology
* Capacitance: 0.2pF typical
* Low clamping voltage
* Low leakage current
* Complies with following standards:
  IEC 61000-4-2 (ESD) immunity test
  Air discharge: ±15KV
  Contact discharge: ±12KV
  IEC61000-4-4 (EFT) 40A (5/50ns)
  IEC61000-4-5 (Lightning) 4A (8/20μs)

■ SYMBOL



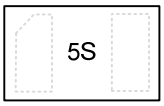
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment		Packing
Lead Free	Halogen Free		1	2	
UESD5V0U1B03L-KAQ-R	UESD5V0U1B03G-KAQ-R	X3DFN0603-2	K	K	Tape Reel

Note: Pin Assignment: K: Cathode

UESD5V0U1B03G-KAQ-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) KAQ: X3DFN0603-2 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
ESD Discharge	IEC61000-4-2	Air Discharge	V_{ESD}	±15	kV
		Contact Discharge		±12	kV
Peak Pulse Current	IEC61000-4-5	t_P=8/20μs	I_{PP}	4	A
Peak Pulse Power			P_{PK}	15	W
Operating Junction Temperature			T_J	-55 ~ +125	°C
Storage Temperature			T_{STG}	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	I_T=0.1mA	6	9.5		V
Reverse Current	I_R	V_R=1.5V			0.1	μA
ESD Dynamic Turn-on Resistance	R_{dyn}	IEC61000-4-2 0 ~ +8kV, Contact mode, T_A=25°C		0.2		Ω
Clamping Voltage	V_{CL}	I_{PPM}=1.0A, t_P=8/20μs		1.75		V
		I_{PPM}=4.0A, t_P=8/20μs		2.9		V
Junction Capacitance	C_J	V_{DC}=2.5V, f=1MHz		0.2	0.3	pF

Note: The strong snap-back to a low holding voltage move into latch-up mode by an ESD event. When designing the Printed-Circuit Board (PCB), give careful consideration to impedance matching and signal coupling. Do not connect the data lines to unlimited DC current sources like, for example, a battery, to avoid the device is “locked” in conducting mode.

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