



MGBR5L100

Preliminary

DIODE

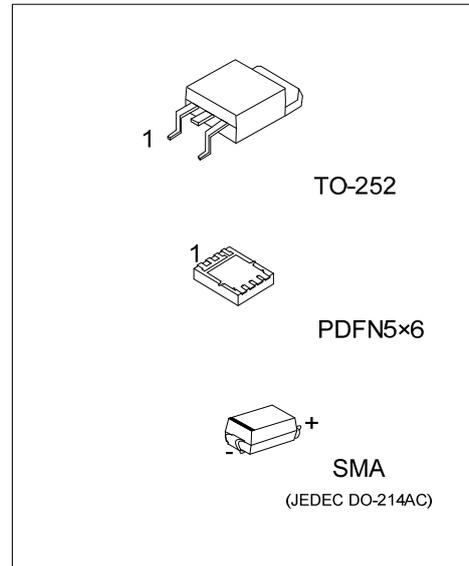
MOS GATED BARRIER RECTIFIER

■ DESCRIPTION

The UTC **MGBR5L100** is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed



■ SYMBOL

SMA	TO-252	PDFN5x6

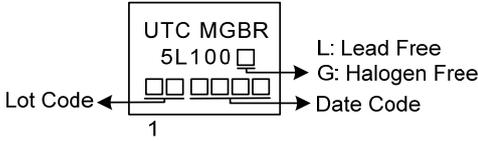
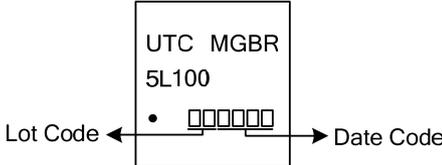
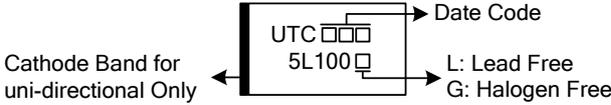
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing	
Lead Free	Halogen Free		1	2	3	4	5	6	7	8		
MGBR5L100L-TN3-R	MGBR5L100G-TN3-R	TO-252	A	K	A	-	-	-	-	-	-	Tape Reel
MGBR5L100L-P5060-R	MGBR5L100G-P5060-R	PDFN5x6	A	A	A	NC	K	K	K	K	-	Tape Reel
MGBR5L100L-SMA-R	MGBR5L100G-SMA-R	SMA	K	A	-	-	-	-	-	-	-	Tape Reel

Note: Pin Assignment: A: Anode K: Cathode NC: No Comment

<p>MGBR5L100G-TN3-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Green Package 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) TN3: TO-252, P5060: PDFN5x6, SMA: SMA (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

Package	MARKING
TO-252	 <p>Diagram showing marking on a TO-252 package. The marking includes 'UTC MGBR' and '5L100'. Below this, there are four boxes representing a lot code and four boxes representing a date code. A '1' is positioned below the date code boxes. To the right, 'L: Lead Free' and 'G: Halogen Free' are indicated. Arrows point from the lot code and date code boxes to their respective labels.</p>
PDFN5x6	 <p>Diagram showing marking on a PDFN5x6 package. The marking includes 'UTC MGBR' and '5L100'. Below this, there is a dot followed by six boxes representing a date code. To the left, 'Lot Code' is indicated with an arrow pointing to the left. To the right, 'Date Code' is indicated with an arrow pointing to the right.</p>
SMA	 <p>Diagram showing marking on an SMA package. The marking includes 'UTC', three boxes, and '5L100'. To the right, 'Date Code' is indicated with an arrow pointing to the right. Below the marking, 'L: Lead Free' and 'G: Halogen Free' are indicated. To the left, 'Cathode Band for uni-directional Only' is indicated with an arrow pointing to the left.</p>

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V _{RM}	100	V
Working Peak Reverse Voltage	V _{RWM}	100	V
Repetitive Peak Reverse Voltage	V _{RRM}	100	V
RMS Reverse Voltage	V _{R(RMS)}	70	V
Average Rectified Output Current	I _O	5	A
T _C =80°C			
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	100	A
Operating Junction Temperature	T _J	-65 ~ +150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA (Note 3)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	32	°C/W
		72	
		75	
Junction to Case	θ _{JC}	2.5	°C/W
		2.4	
		35	

■ ELECTRICAL CHARACTERISTICS(T_A=25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.5mA	100			V
Forward Voltage Drop	V _{FM}	I _F =5A, T _J =25°C			0.80	V
		I _F =5A, T _J =125°C			0.75	V
Leakage Current (Note 1)	I _{RM}	V _R =100V, T _J =25°C			250	μA
		V _R =100V, T _J =125°C			25	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

3. Mounted on an FR4 PCB, single-sided copper, with 100 cm² copper pad area.

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