



F24NM60

Power MOSFET

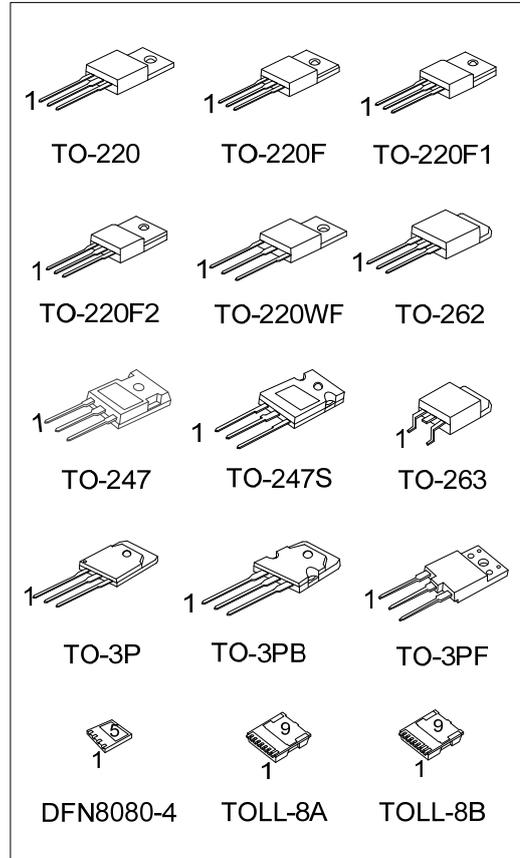
24A, 600V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

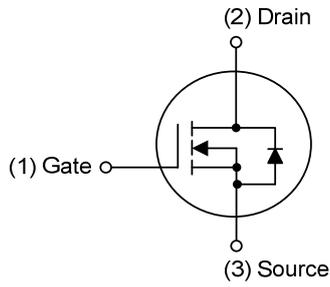
The **UTC F24NM60** is a N-Channel enhancement mode silicon gate power MOSFET with Fast Body Diode. is designed high voltage, high speed power switching applications such. such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics.

FEATURES

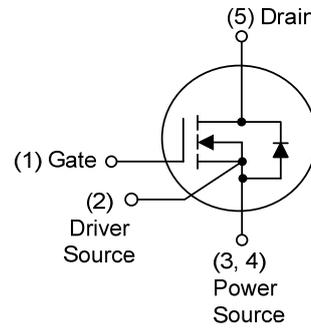
- * $R_{DS(ON)} \leq 0.16 \Omega @ V_{GS}=10V, I_D=12A$
- * Fast body diode MOSFET technology
- * Low switching losses due to reduced Q_{rr}
- * Single Pulse Avalanche Energy Rated
- * Fast Switching Speeds
- * Linear Transfer Characteristics
- * High Input Impedance
- * Avalanche energy tested



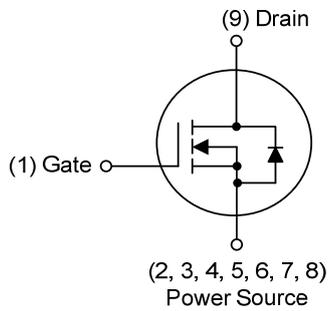
■ SYMBOL



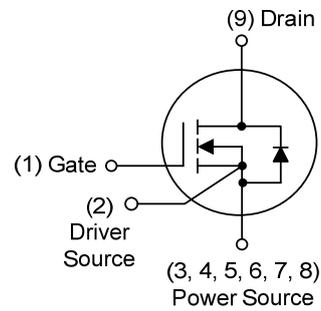
TO-220 / TO-220F / TO-220F1 / TO-220F2
 TO-220WF / TO-3P / TO-3PB / TO-3PF
 TO-247 / TO-247S / TO-262 / TO-263



DFN8080-4



TOLL-8A



TOLL-8B

ORDERING INFORMATION

Ordering Number		Package	Pin Assignment									Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	9	
F24NM60L-TA3-T	F24NM60G-TA3-T	TO-220	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TF1-T	F24NM60G-TF1-T	TO-220F1	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TF2-T	F24NM60G-TF2-T	TO-220F2	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TF3-T	F24NM60G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TW1-T	F24NM60G-TW1-T	TO-220WF	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-T2Q-T	F24NM60G-T2Q-T	TO-262	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TQ2-T	F24NM60G-TQ2-T	TO-263	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-TQ2-R	F24NM60G-TQ2-R	TO-263	G	D	S	-	-	-	-	-	-	Tape Reel
F24NM60L-T3P-T	F24NM60G-T3P-T	TO-3P	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-T3B-T	F24NM60G-T3B-T	TO-3PB	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-T3F-T	F24NM60G-T3F-T	TO-3PF	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-T47-T	F24NM60G-T47-T	TO-247	G	D	S	-	-	-	-	-	-	Tube
24NM65L-T47S-T	24NM65G-T47S-T	TO-247S	G	D	S	-	-	-	-	-	-	Tube
F24NM60L-K04-8080-R	F24NM60G-K04-8080-R	DFN8080-4	G	S	S	S	D	-	-	-	-	Tape Reel
F24NM60L-T8A-R	F24NM60G-T8A-R	TOLL-8A	G	S	S	S	S	S	S	S	D	Tape Reel
F24NM60L-T8B-R	F24NM60G-T8B-R	TOLL-8B	G	S	S	S	S	S	S	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>F24NM60G-TA3-T</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TW1: TO-220WF, T2Q: TO-262, TQ2: TO-263, T3P: TO-3P, T3B: TO-3PB, T3F: TO-3PF, T47: TO-247, T47S: TO-247S, K04-8080: DFN8080-4, T8A: TOLL-8A, T8B: TOLL-8B</p> <p>(3) G: Halogen Free and Lead Free L: Lead Free</p>
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MARKING

TO-220 / TO-220F / TO-220F1 / TO-220F2 TO-220WF / TO-3P / TO-3PB / TO-3PF TO-247 / TO-247S / TO-262 / TO-263	DFN8080-4
<p>UTC F24NM60</p> <p>Lot Code ← → Date Code</p> <p>1</p> <p>L: Lead Free G: Halogen Free</p>	<p>UTC F24NM60</p> <p>Lot Code ← → Date Code</p>
TOLL-8A / TOLL-8B	-
<p>UTC F24NM65</p> <p>Lot Code ← → Date Code</p> <p>1</p> <p>L: Lead Free G: Halogen Free</p>	-

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	24	A
	Pulsed (Note 2)	I_{DM}	48	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	984	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	13.4	V/nS
Power Dissipation	TO-220/TO-262 TO-263	P_D	120	W
	TO-220F/TO-220F1 TO-220F2/TO-220WF		36	W
	TO-3P/TO-3PB		155	W
	TO-3PF		58	W
	TO-247/TO-247S		140	W
	DFN8080-4		66	W
	TOLL-8A/TOLL-8B		205	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 60 \text{ mH}$, $I_{AS} = 5.7\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 24\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220WF/TO-262 TO-263	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
	TO-3P/TO-3PB TO-3PF		30	$^\circ\text{C}/\text{W}$
	TO-247/TO-247S		40	$^\circ\text{C}/\text{W}$
	DFN8080-4/TOLL-8A TOLL-8B		35 (Note)	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220/TO-262 TO-263	θ_{JC}	1.04	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1 TO-220F2/TO-220WF		3.47	$^\circ\text{C}/\text{W}$
	TO-3P/TO-3PB		0.8	$^\circ\text{C}/\text{W}$
	TO-3PF		2.155	$^\circ\text{C}/\text{W}$
	TO-247/TO-247S		0.89	$^\circ\text{C}/\text{W}$
	DFN8080-4		1.89 (Note)	$^\circ\text{C}/\text{W}$
	TOLL-8A/TOLL-8B		0.6 (Note)	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

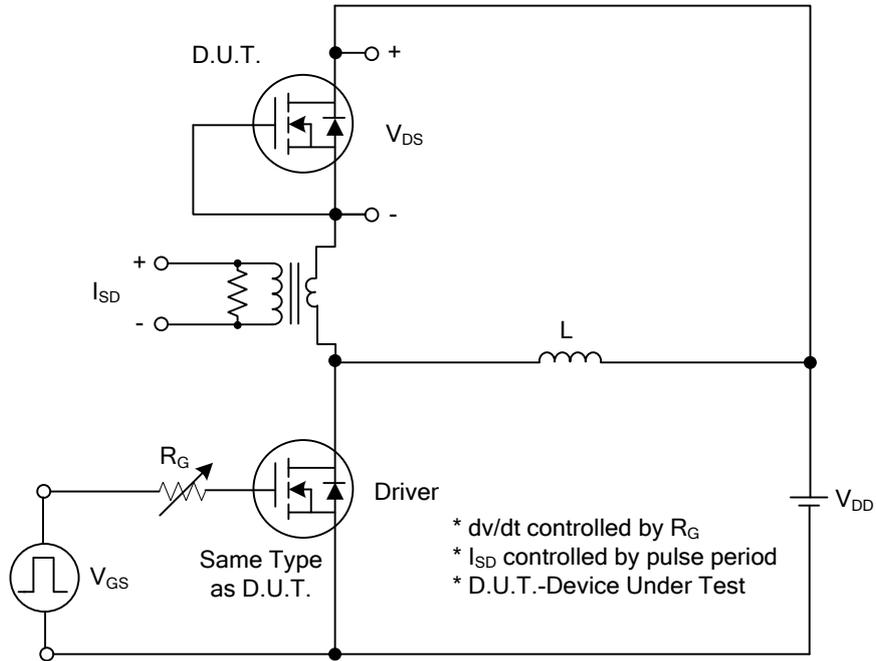
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	600			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	I _{GSS}			+100	nA
	Reverse					
		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =12A			0.16	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		1980		pF
Output Capacitance	C _{OSS}			1590		pF
Reverse Transfer Capacitance	C _{RSS}			140		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =480V, V _{GS} =10V, I _D =24A I _G =1mA (Note1, 2)		77		nC
Gate to Source Charge	Q _{GS}			30		nC
Gate to Drain Charge	Q _{GD}			33		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =24A, R _G =25Ω (Note1, 2)		32		ns
Rise Time	t _R			38		ns
Turn-OFF Delay Time	t _{D(OFF)}			218		ns
Fall-Time	t _F			80		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				24	A
Maximum Body-Diode Pulsed Current	I _{SM}				48	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =24A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =24A, V _{GS} =0V, dI _F /dt=100A/μs		290		ns
Body Diode Reverse Recovery Charge	Q _{rr}				3.5	

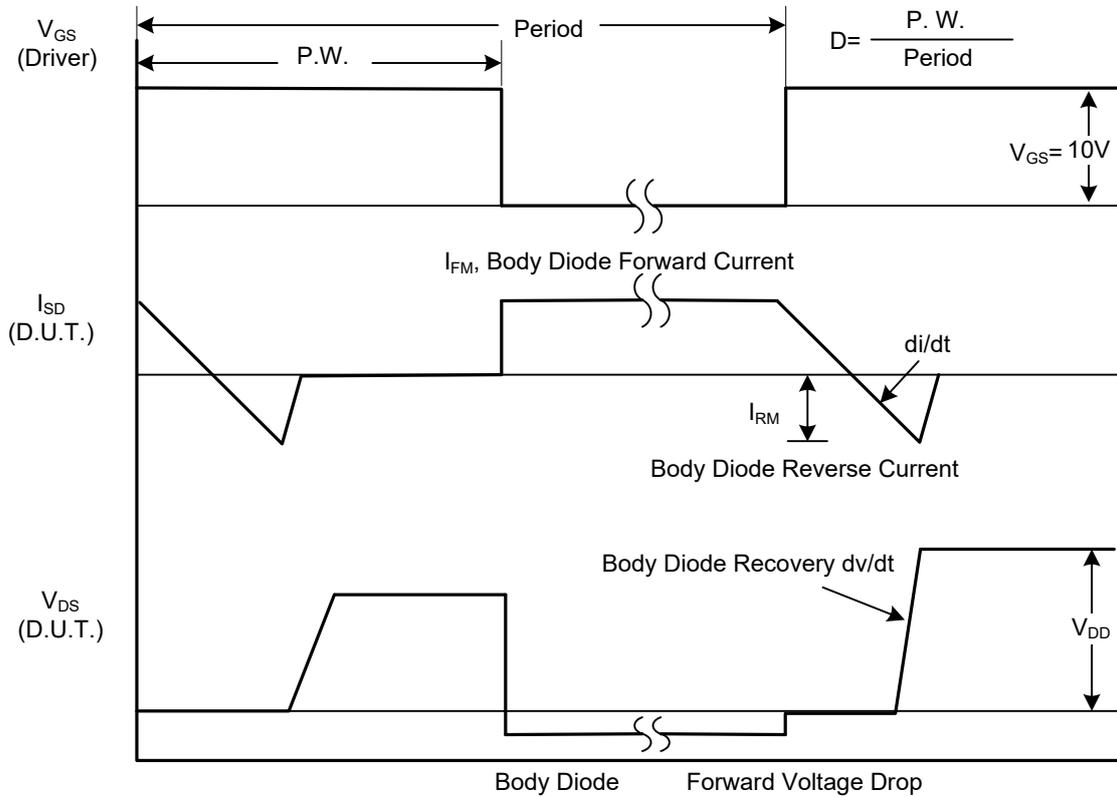
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

TEST CIRCUITS AND WAVEFORMS



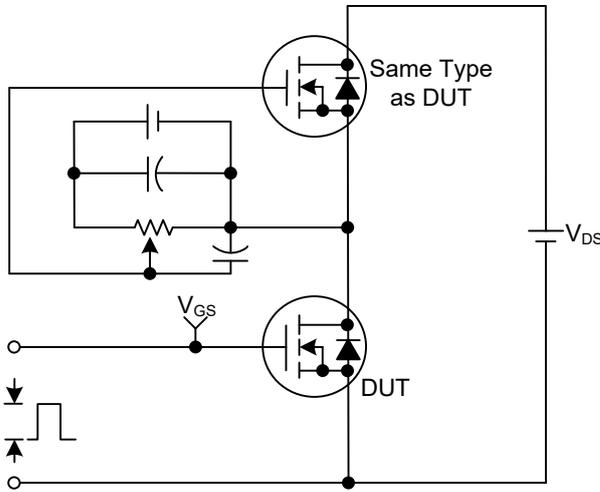
Peak Diode Recovery dv/dt Test Circuit



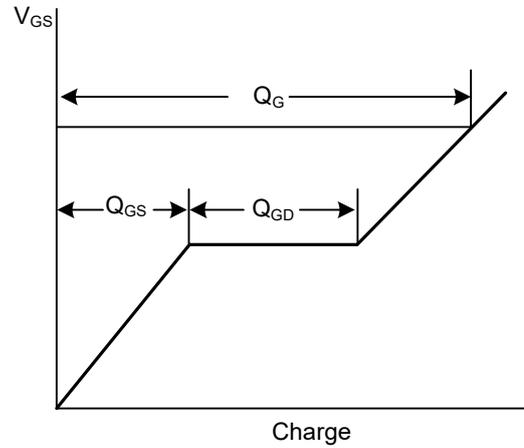
Peak Diode Recovery dv/dt Waveforms

TEST CIRCUITS AND WAVEFORMS

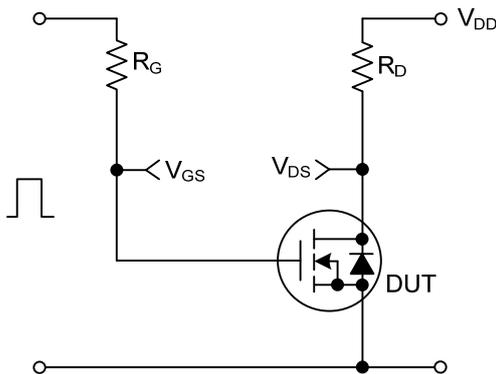
Gate Charge Test Circuit



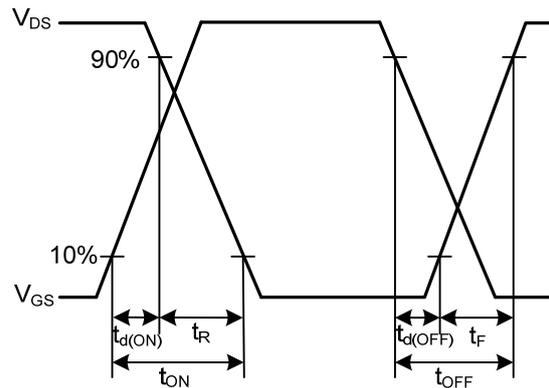
Gate Charge Waveforms



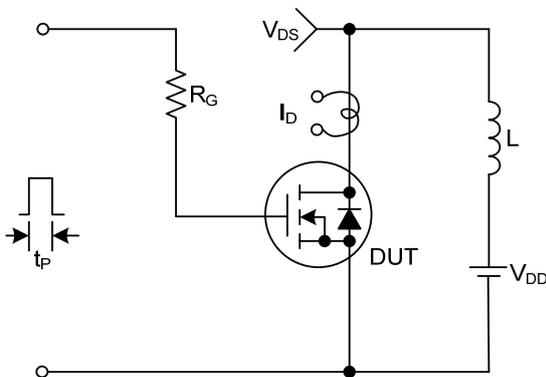
Resistive Switching Test Circuit



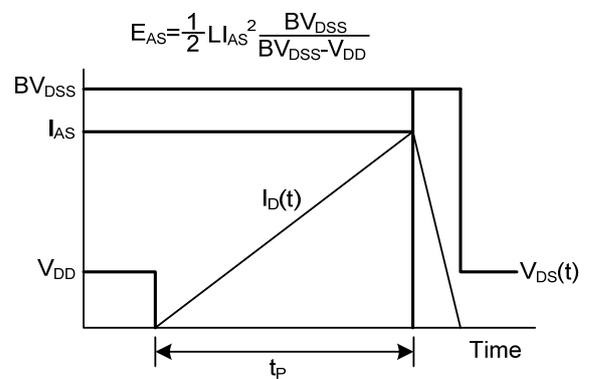
Resistive Switching Waveforms



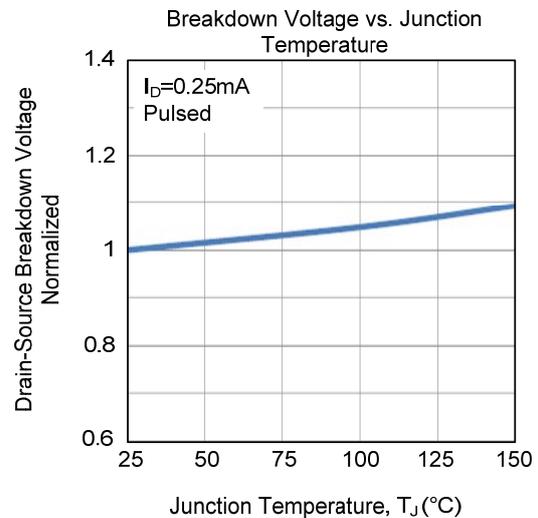
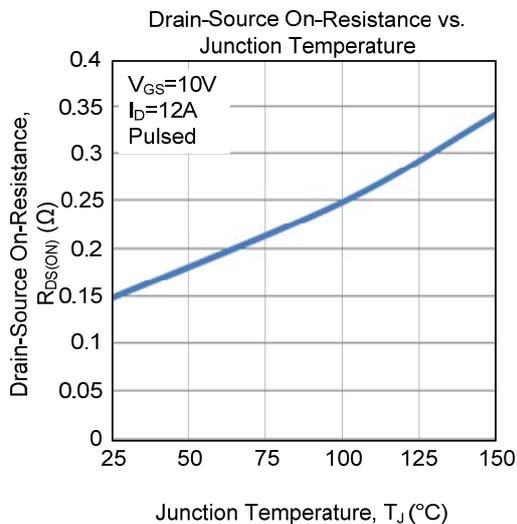
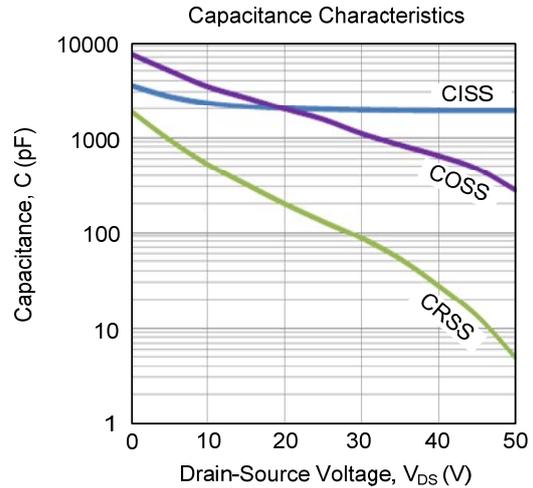
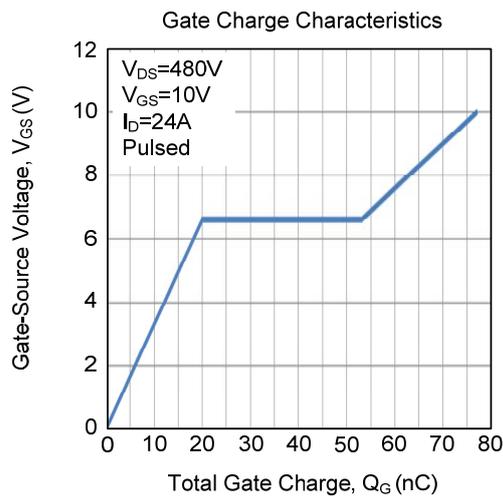
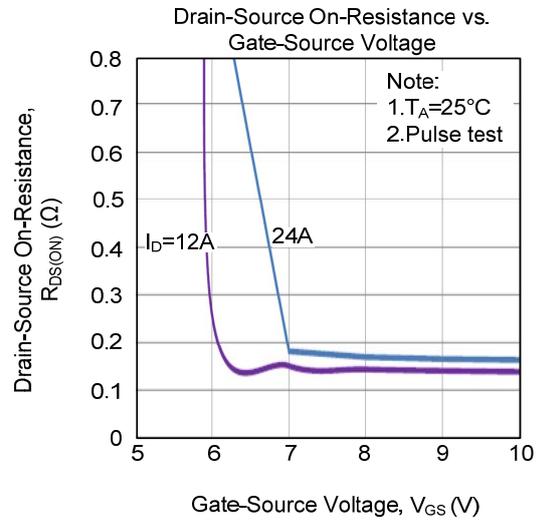
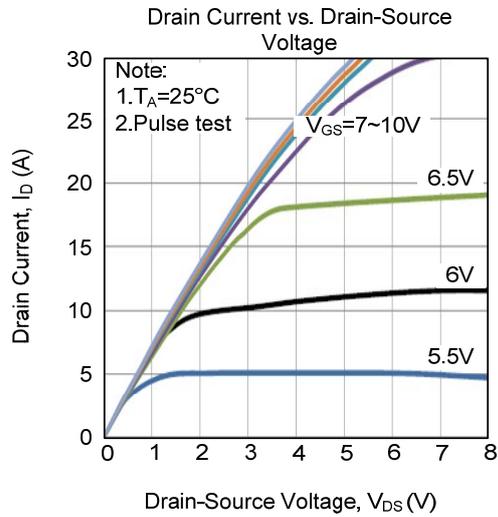
Unclamped Inductive Switching Test Circuit



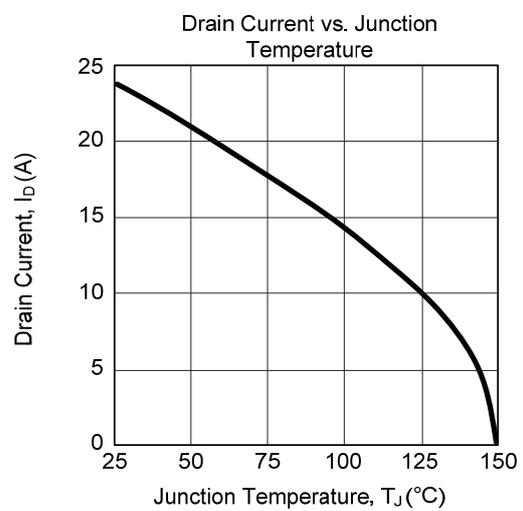
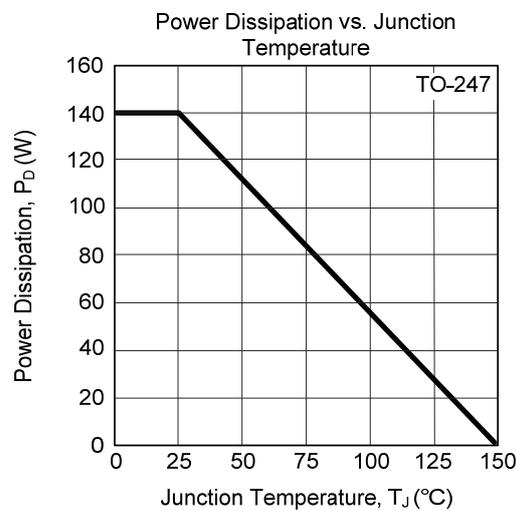
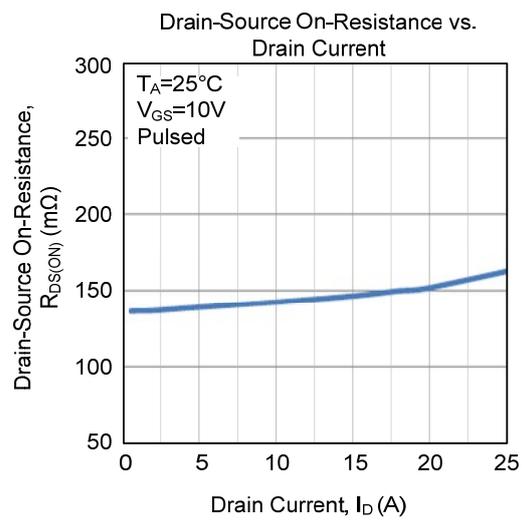
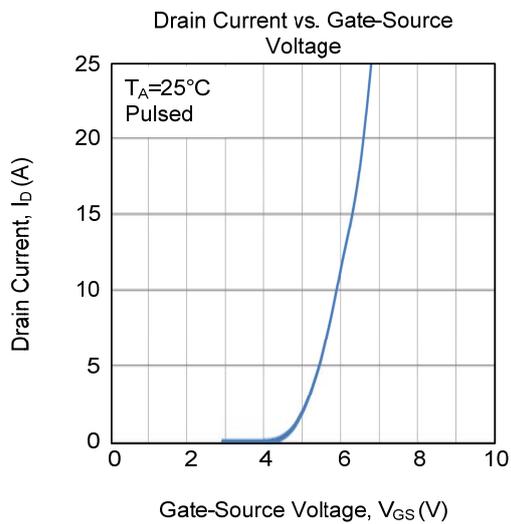
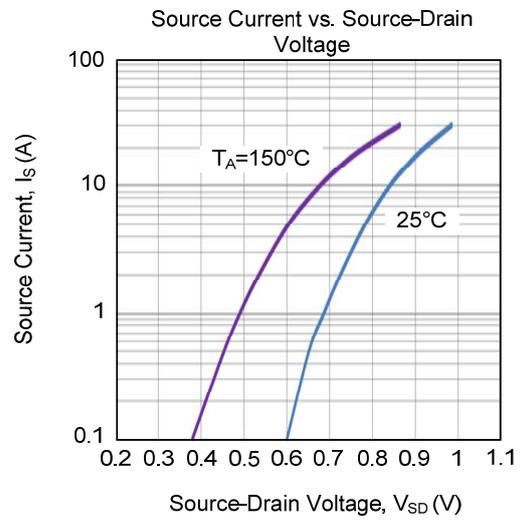
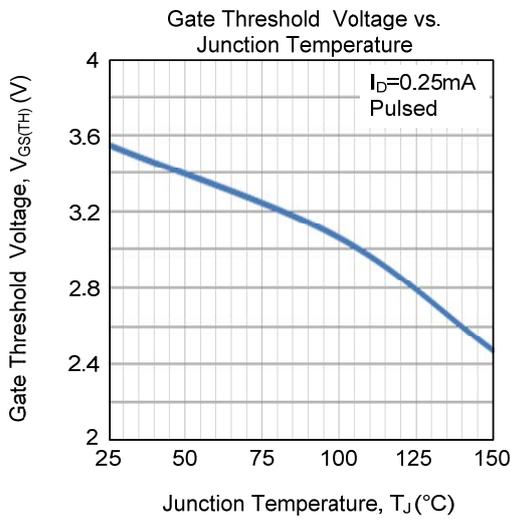
Unclamped Inductive Switching Waveforms



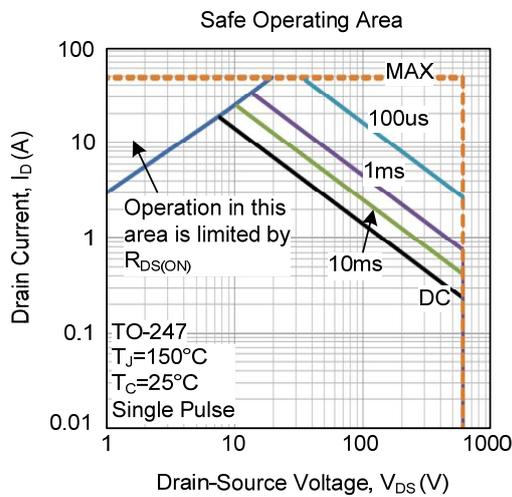
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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