

UNISONIC TECHNOLOGIES CO., LTD

F21NM50 Preliminary Power MOSFET

21A, 500V N-CHANNEL SUPER-JUNCTION MOSFET

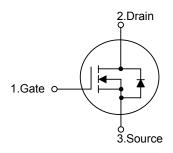
■ DESCRIPTION

The **UTC F21NM50** is a N-Channel enhancement mode silicon gate super junction power MOSFET with fast body diode and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)} \le 0.23 \Omega @ V_{GS} = 10V, I_D = 10.5A$
- * Fast body diode MOSFET technology
- * High Switching Speed

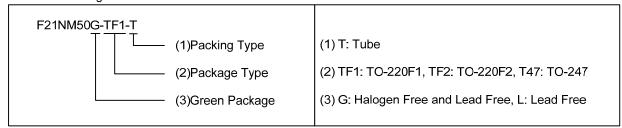
■ SYMBOL



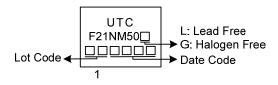
ORDERING INFORMATION

Ordering Number		Dealtons	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
F21NM50L-TF1-T	F21NM50G-TF1-T	TO-220F1	G	D	S	Tube	
F21NM50L-TF2-T	F21NM50G-TF2-T	TO-220F2	G	D	S	Tube	
F21NM50L-T47-T	F21NM50G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



MARKING



TO-220F1

TO-220F2

TO-247

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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	500	V	
Gate-Source Voltage		V_{GSS}	± 30	V	
Drain Current	Continuous	I_D	21	Α	
	Pulsed (Note 2)	I_{DM}	42	Α	
Avalanche Energy	lanche Energy Single Pulsed (Note 3)		614	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	12.1	V/ns	
Power Dissipation	TO-220F1/TO-220F2	ר	30	W	
	TO-247	P _D	186	W	
Junction Temperature		T_J	+150	°C	
Storage Temperature		T_{STG}	-55 ~ + 150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 66mH, I_{AS} = 4.3A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 21A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F1/TO-220F2	0	62.5	°C/W
	TO-247	θ_{JA}	50	°C/W
Junction to Case	TO-220F1/TO-220F2	0	4.16	°C/W
	TO-247	θ_{JC}	0.67	°C/W

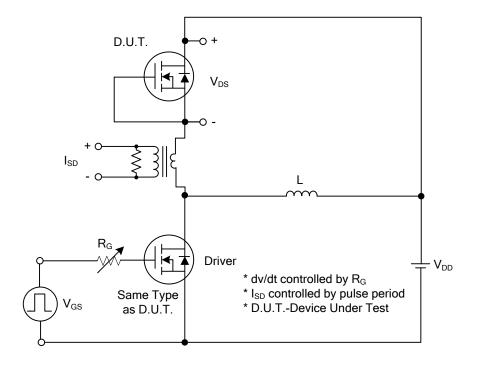
■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS} =0V, I_{D} = 250 μ A	500			V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =500V, V _{GS} =0V			10	μΑ	
Gate-Source Leakage Current	Forward	ı	V _{GS} =30V, V _{DS} =0V			100	nA	
	Reverse	I _{GSS}	V_{GS} =-30V, V_{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	2.5		4.5	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =10.5A			0.23	Ω	
DYNAMIC CHARACTERISTICS								
nput Capacitance		C _{ISS}			1146		pF	
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0 MHz		916.3		pF	
Reverse Transfer Capacitance		C_{RSS}			98.1		pF	
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_G	V _{DS} =400V, V _{GS} =10V, I _D =21A		47.5		nC	
Gate-Source Charge		Q_{GS}	I_{G} =1mA (Note 1, 2)		14		nC	
Gate-Drain Charge		Q_GD	IG-IIIIA (Note 1, 2)		17.1		nC	
Turn-on Delay Time (Note 1)		t _{D(ON)}			10.5		ns	
Rise Time		t_R	V _{DS} =100V, V _{GS} =10V, I _D =21A,		18.7		ns	
Turn-off Delay Time		t _{D(OFF)}	R _G =6Ω (Note 1, 2)		45.2		ns	
Fall-Time		t_{F}			22.1		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous Current		I _S				21	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				42	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	V _{GS} =0V, I _S =21A			1.4	V	
Reverse Recovery Time (Note 1)		t _{rr}	V _{GS} =0V, I _S =21A,		204.5		ns	
Reverse Recovery Charge		Q_{rr}	dI _F /dt=100A/μs (Note1)		3.2		μC	

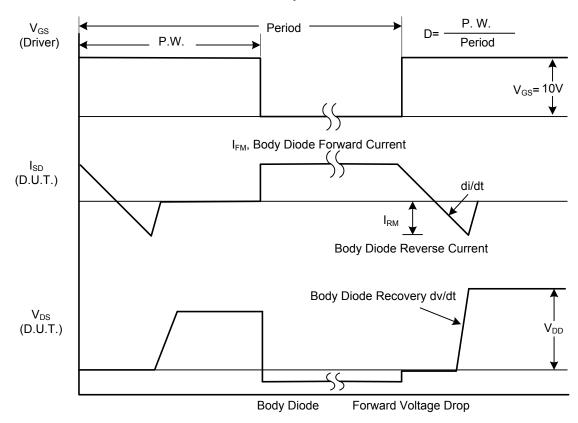
Notes: 1. Pulse Test : Pulse width \leq 300 μ s, Duty cycle \leq 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

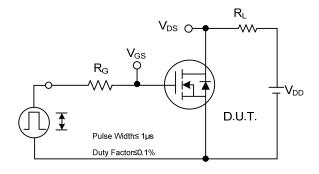


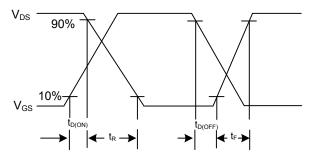
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

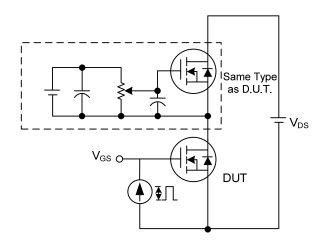
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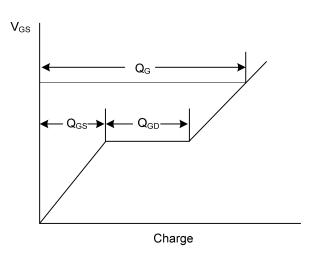




Switching Test Circuit

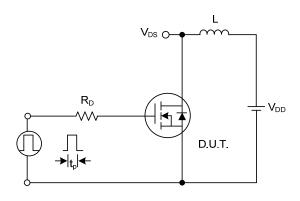
Switching Waveforms

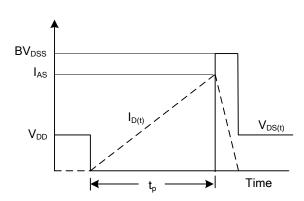




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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