

# UNISONIC TECHNOLOGIES CO., LTD

BTB320A Preliminary TRIAC

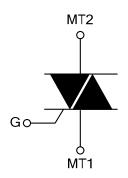
## 20A TRIACS

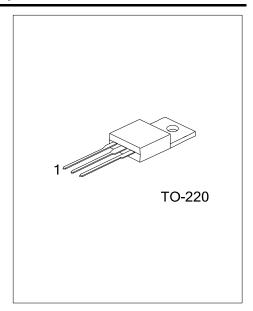
#### DESCRIPTION

The UTC **BTB320A** is a 20A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC **BTB320A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

#### ■ SYMBOL

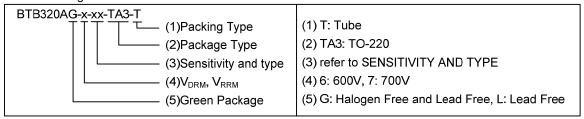




## ORDERING INFORMATION

Ordering	Dookogo	Pin .	Assignn	Dooking		
Lead Free Halogen Free		Package	1	2	3	Packing
BTB320AL-x-xx-TA3-T	BTB320AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube

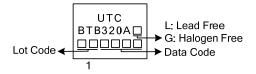
Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate



#### SENSITIVITY AND TYPE

PART NUMBER		ak off-state voltage RM, V <sub>RRM</sub> )	SENSITIVITY	TYPE	
	600V	700V			
BW		0	50mA	SNUBBERLESS	
CW	0	0	35mA	SNUBBERLESS	

#### MARKING



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#### ABSOLUTE MAXIMUM RATINGS

PAR	AMETER	SYMBOL	RATINGS	UNIT	
RMS On-State Current (Full Sine Wave)		T <sub>C</sub> =70°C	I <sub>T(RMS)</sub>	20	Α
Non Repetitive Surge Peak On-State Current	F=50 Hz	t=10ms	I <sub>TSM</sub>	210	Α
(Full Cycle, T <sub>J</sub> initial=25°C)	F=60 Hz	t=8.3ms		200	Α
I <sup>2</sup> t Value for Fusing	t <sub>P</sub> =10ms		l <sup>2</sup> t	200	$A^2s$
Critical Rate of Rise of	Repetitive, F=50 Hz			50	A/µs
On-State Current I <sub>G</sub> =500mA, dI <sub>G</sub> /dt =1A/µs	Non Repetitive	T <sub>J</sub> =125°C   dl/dt		100	A/µs
Non Repetitive Surge Peak Off-State Voltage	t <sub>P</sub> =10ms	TJ=25°C	$V_{DSM}/V_{RSM}$	V <sub>DSM</sub> /V <sub>RSM</sub> +100	V
Peak Gate Current	t <sub>P</sub> =20µs T <sub>J</sub> =125°C		$I_{GM}$	4	Α
Peak Positive Gate Voltage t <sub>P</sub> =20µs			$V_{GM}$	16	V
Average Gate Power Dissipation T <sub>J</sub> =125°C		T <sub>J</sub> =125°C	P <sub>G(AV)</sub>	1	W
Operating Junction Temperature			$T_J$	-40~+125	°C
Storage Junction Temperature			T <sub>STG</sub>	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	60	°C/W
Junction to Case (AC)	0	1.3	°C/W
Junction to Case (DC)	θ <sub>JC</sub>	1.7	°C/W

## ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C unless otherwise specified.)

DADAMETED	0)////DOI	TEST CONDITIONS		BW			CW			
PARAMETER	SYMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	$I_{GT}$	V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	1-11-111	2		50	1		35	mA
Gate Trigger Voltage	$V_{GT}$	VD-12V, INL-0032	1-11-111			1.5			1.5	V
Gate Non-Trigger Voltage	$V_{GD}$	$V_D = V_{DRM}$ , $R_L = 3.3k\Omega$ , $T_J = 125^{\circ}C$	1-11-111	0.2			0.2			٧
Holding Current (Note 2)	I <sub>H</sub>	I <sub>T</sub> =500mA, Gate Open				75			50	mΑ
			1-111		50					mΑ
Latching Current	IL	I <sub>G</sub> =1.2I <sub>GT</sub>	II		90					mΑ
			1-11-111						80	mΑ
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , Gate Open, T <sub>J</sub> =125°C		500	750		250	500		V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dV/dt)c	(dl/dt)c=20A/ms, T <sub>J</sub> =125°C		18	36		11	22		V/µs

## ■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	$V_{TM}$	I <sub>TM</sub> =28A, t <sub>p</sub> =380μs	T <sub>J</sub> =25°C			1.70	V
Repetitive Peak Off-State	I <sub>DRM</sub>	\/ -\/	T <sub>J</sub> =25°C			10	μΑ
Current	I <sub>RRM</sub>	$V_{DRM}=V_{RRM}$	T <sub>J</sub> =125°C			3	mA

Notes: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

2. For both polarities of MT2 referenced to MT1.

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