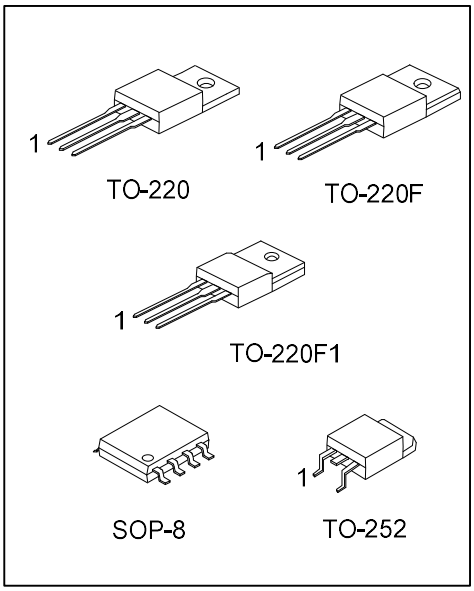
7N65-LD

Preliminary | ***Power MOSFET***

7.0A, 650V N-CHANNEL POWER MOSFET



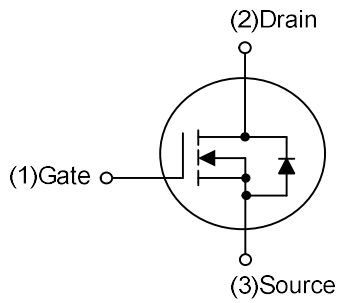
■ DESCRIPTION

The UTC **7N65-LD** is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

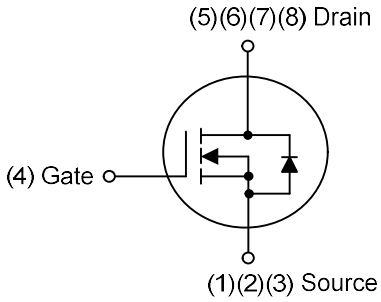
■ FEATURES

* $R_{DS(ON)} \leq 1.7\ \Omega$ @ V_{GS} = 10V, I_D = 3.5A
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

■ SYMBOL



TO-220 / TO-220F / TO-220F1 / TO-252

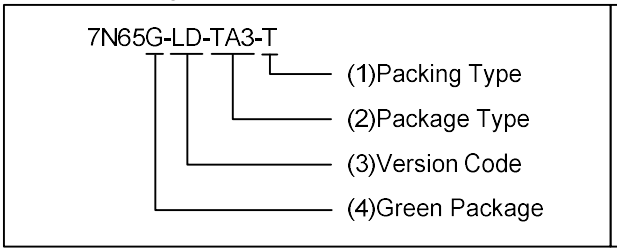


SOP-8

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
7N65L-LD-TA3-T	7N65G-LD-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
7N65L-LD-TF3-T	7N65G-LD-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
7N65L-LD-TF1-T	7N65G-LD-TF1-T	TO-220F1	G	D	S	-	-	-	-	-	Tube
7N65L-LD-TN3-R	7N65G-LD-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
7N65L-LD-S08-R	7N65G-LD-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

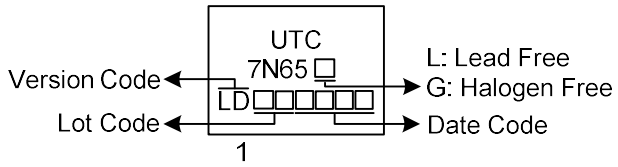
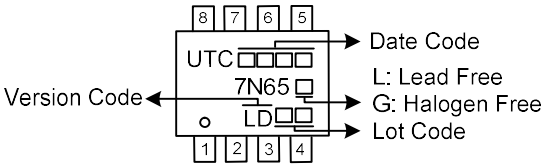
Note: Pin Assignment: G: Gate D: Drain S: Source



(1) T: Tube, R: Tape Reel
(2) TA3: TO-220, TF1: TO-220F1, TF3: TO-220F
TN3: TO-252, S08: SOP-8
(3) Version ML
(4) G: Halogen Free and Lead Free, L: Lead Free

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■ MARKING

TO-220 / TO-220F / TO-220F1 / TO-252	SOP-8
	

■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current		I_D	7	A
Pulsed Drain Current (Note 2)		I_{DM}	14	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	264	mJ
Power Dissipation	TO-220	P_D	125	W
	TO-220F/TO-220F1		35	W
	TO-252		48	W
	SOP-8		2.5	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 30mH, I_{AS} = 4.2A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1	θ_{JA}	62.5	°C/W
	TO-252		110	°C/W
	SOP-8		190	°C/W
Junction to Case	TO-220	θ_{JC}	1	°C/W
	TO-220F/TO-220F1		3.57	°C/W
	TO-252		2.6 (Note)	°C/W
	SOP-8		50 (Note)	°C/W

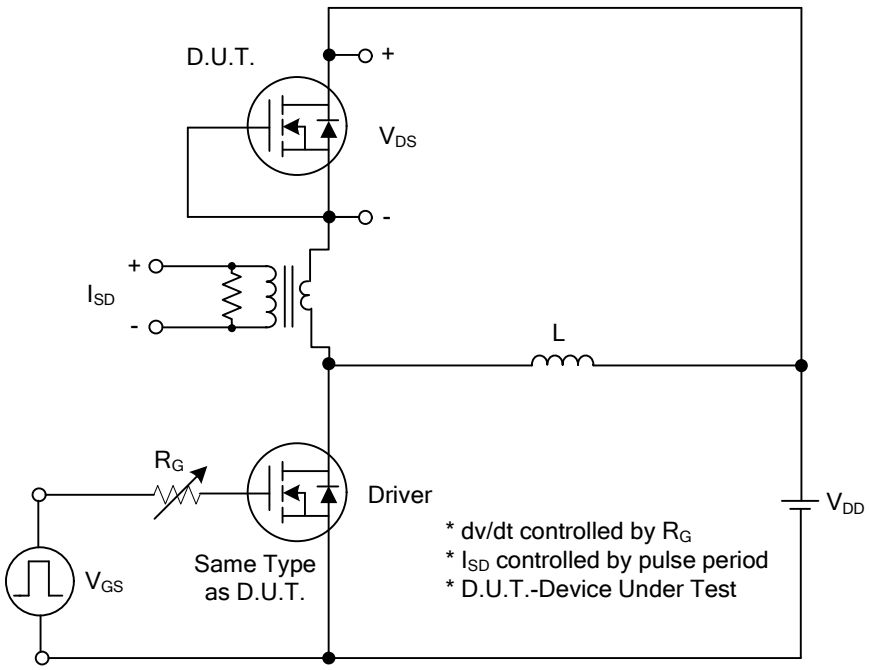
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

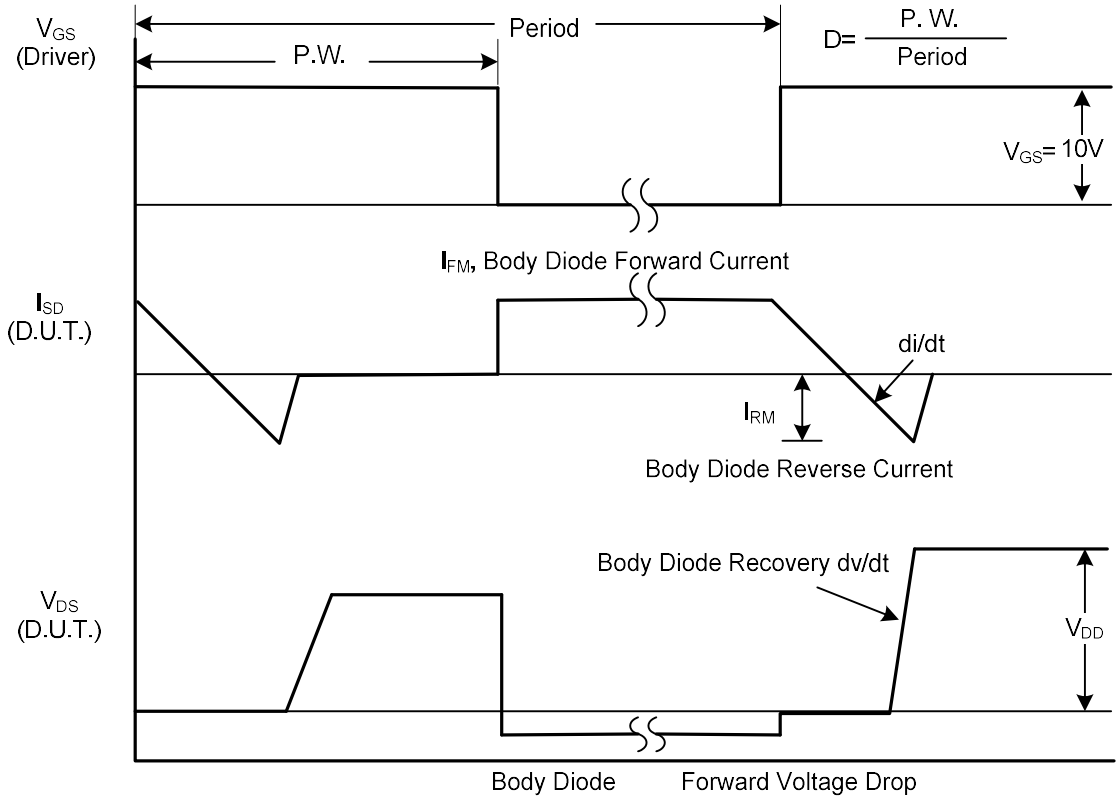
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	650			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=650V, V_{GS}=0V			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=3.5A			1.7	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1.0MHz		888		pF
Output Capacitance		C_{OSS}			87		pF
Reverse Transfer Capacitance		C_{RSS}			6		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=520V, V_{GS}=10V, I_D=7.0A I_G=1mA (Note 1, 2)		24		nC
Gate-Source Charge		Q_{GS}			8		nC
Gate-Drain Charge		Q_{GD}			2		nC
Turn-On Delay Time (Note 1)		$t_{D(ON)}$	V_{DS}=100V, V_{GS}=10V, I_D=7.0A, R_G=25Ω (Note 1, 2)		10		ns
Turn-On Rise Time		t_R			17		ns
Turn-Off Delay Time		$t_{D(OFF)}$			57		ns
Turn-Off Fall Time		t_F			29		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I_S				7	A
Maximum Body-Diode Pulsed Current		I_{SM}				14	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=7.0A , V_{GS}=0V			1.4	V

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

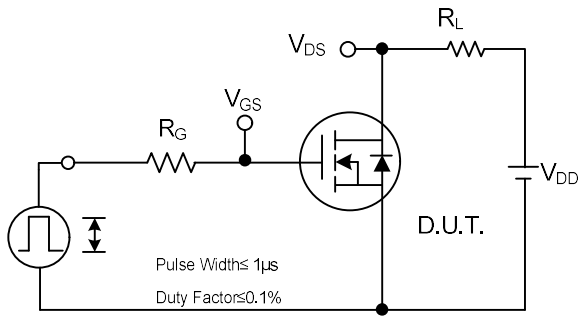


Peak Diode Recovery dv/dt Test Circuit

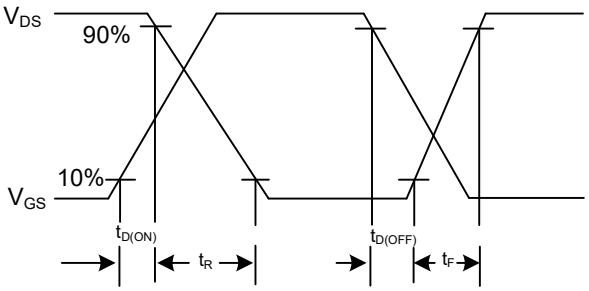


Peak Diode Recovery dv/dt Waveforms

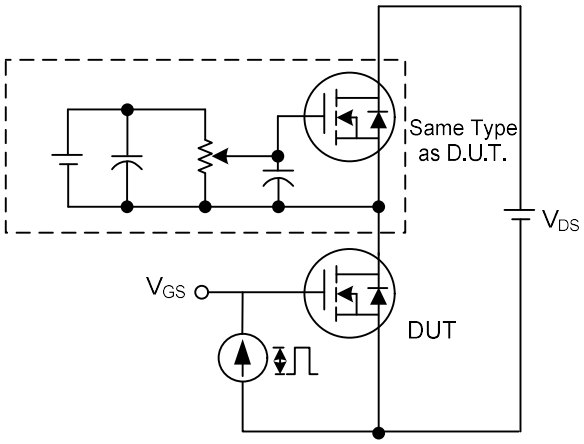
■ TEST CIRCUITS AND WAVEFORMS



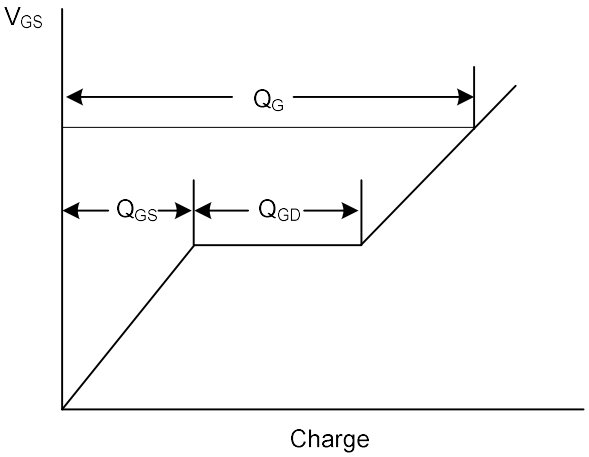
Switching Test Circuit



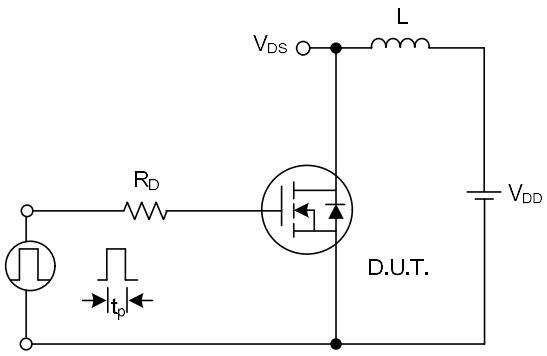
Switching Waveforms



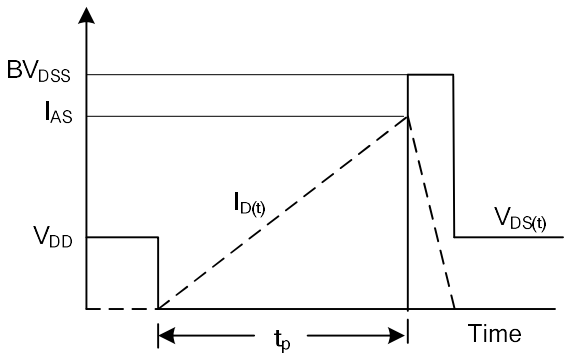
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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