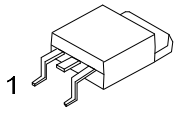
2NK150Z **Preliminary** ***POWER MOSFET***

2.0A, 1500V N-CHANNEL POWER MOSFET



TO-252

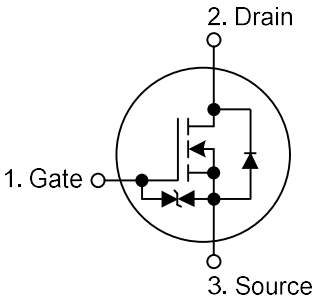
■ DESCRIPTION

The UTC **2NK150Z** is a silicon N-channel MOSFET, it uses UTC's advanced technology to provide the customers with a minimum on state resistance, high switching speed and low gate charge.

■ FEATURES

* $R_{DS(ON)} \leq 12.5\ \Omega$ @ V_{GS} = 10V, I_D = 1.0A
* High switching speed
* Low input capacitance
* With ESD protection
* Green Product

■ SYMBOL



■ ORDERING INFORMATION

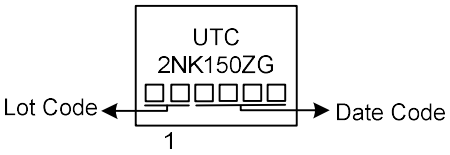
Ordering Number	Package	Pin Assignment			Packing
		1	2	3	
2NK150ZG-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

2NK150ZG-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free

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■ MARKING



■ ABSOLUTE MAXIMUM RATING (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	1500	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	DC	I_D	2	A
	Pulsed (Note 2)	I_{DM}	6	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	128	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.1	V/ns
Power Dissipation		P_D	54	W
Junction Temperature		T_J	+150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 100mH, I_{AS} = 1.6A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C.
4. I_{SD} ≤ 2.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J=25°C.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	110	°C/W
Junction to Case	θ_{JC}	2.31 (Note)	°C/W

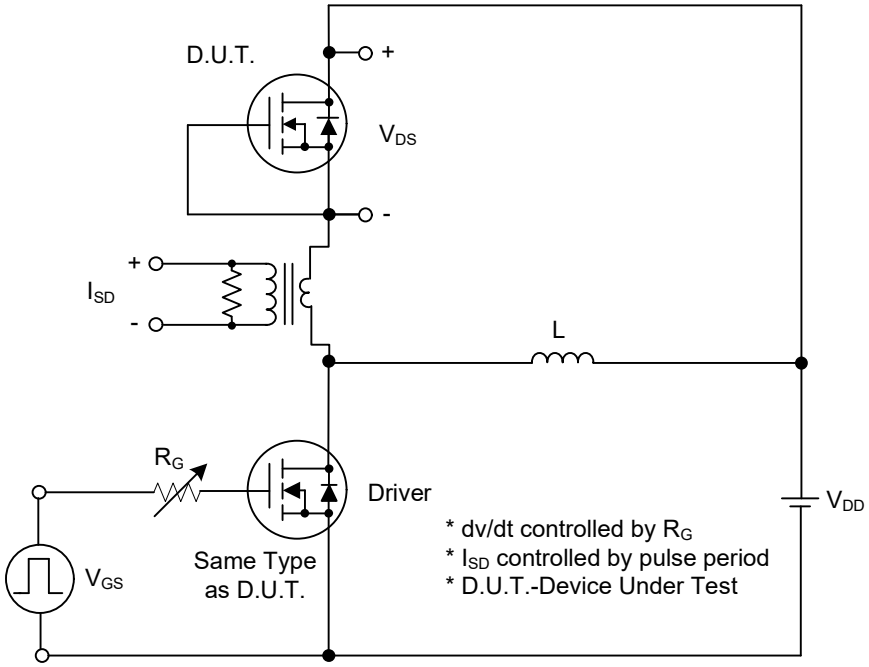
Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise specified)

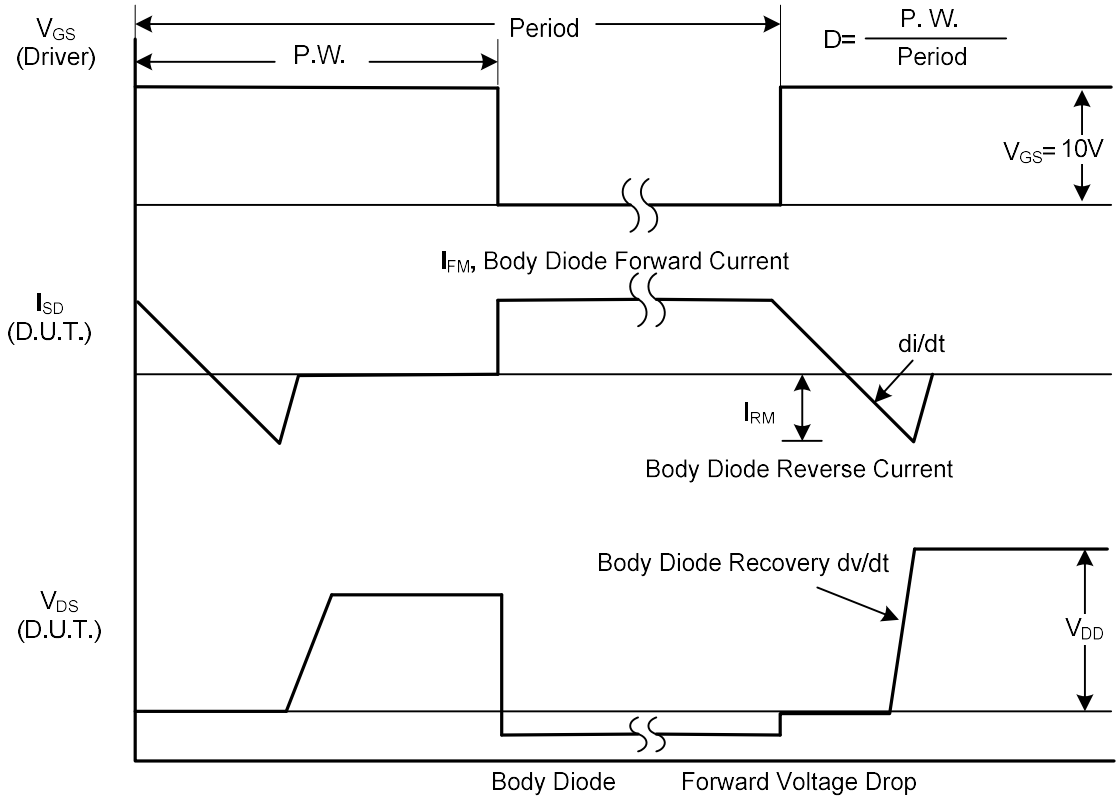
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	1500			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=1500V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+10	μA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-10	μA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	3.0		5.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=1.0A			12.5	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		927		pF
Output Capacitance		C_{OSS}			63		pF
Reverse Transfer Capacitance		C_{RSS}			3		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{DS}=1200V, V_{GS}=10V, I_D=2.0A (Note 1, 2)		23		nC
Gate to Source Charge		Q_{GS}			9		nC
Gate to Drain Charge		Q_{GD}			5		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V, I_D=2.0A, R_G=25Ω (Note 1, 2)		20		ns
Rise Time		t_R			20		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			49		ns
Fall-Time		t_F			47		ns
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode Forward Current		I_S				2	A
Maximum Pulsed Drain-Source Diode Forward Current		I_{SM}				6	A
Diode Forward Voltage		V_{SD}	I_F=2.0A, V_{GS}=0V			1.4	V
Reverse Recovery Time		t_{rr}	I_S=2.0A, V_{GS}=0V, dI_F/dt = 100 A/μs		908		ns
Reverse Recovery Charge (Note 1)		Q_{rr}			6.22		uC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

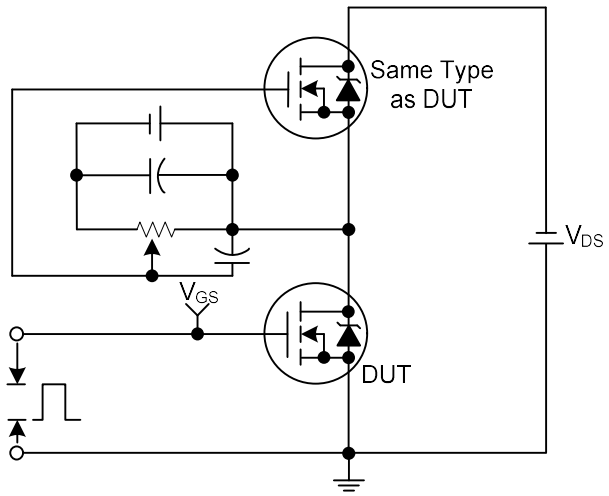


Peak Diode Recovery dv/dt Test Circuit

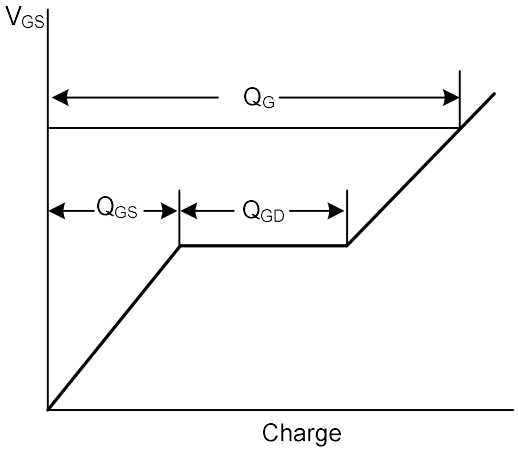


Peak Diode Recovery dv/dt Waveforms

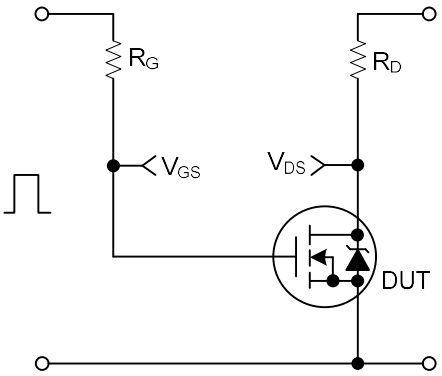
■ TEST CIRCUITS AND WAVEFORMS



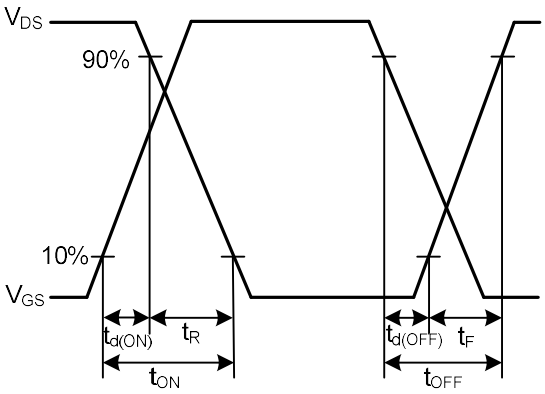
Gate Charge Test Circuit



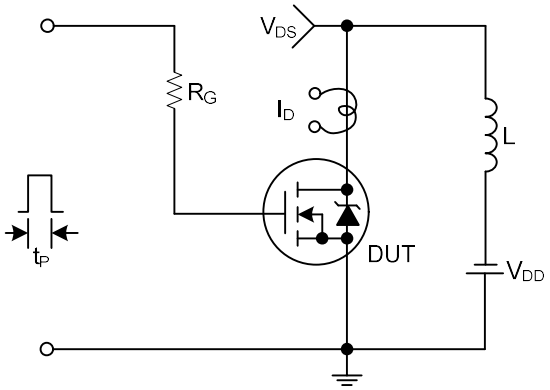
Gate Charge Waveforms



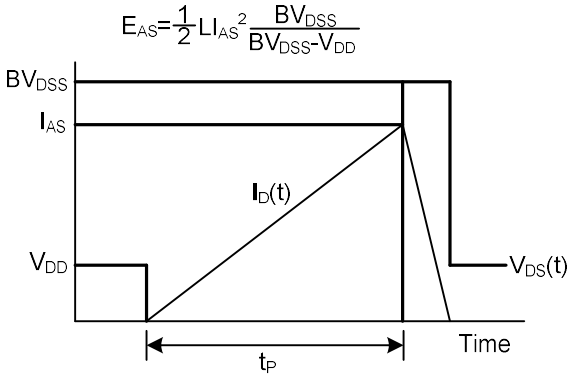
Resistive Switching Test Circuit



Resistive Switching Waveforms



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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